

SoM-200 Pin Specification

Revision 3.0

04/25/2012

Revision Log:

08/20/2009 New specification for 200 pin SoM to include Gig Ethernet & USB power control.  
 12/22/2010 Switched SPI Lines on SoM-9G45/9M10  
 01/10/2010 Substituted NCS2 for NCS5 on SoM-9G45/9M10  
 04/25/2012 Added SoM-3517 updated pin-out

	PIN#	Module Pin Name	Cirrus 9307	BGA Pin#	Comment	Atmel 9G45/9M10	Port Line	BGA Pin#	Comment	TI AM3517	Port Line	BGA Pin#	Comment	
1	<b>System Control</b>													
2	145	GP_CSA	-CS[0]	J16		NCS0		A10		GPMC_NCS1	GPIO_52	L1		
3	146	GP_CSB	-CS[1]/Wtchdog	A1		NCS4	PC10	B6		GPMC_NCS2	GPIO_53	M4		
4	147	GP_CSC	-CS[2]/Reset	B2		NCS2	PC13	D7	was NCS5	GPMC_NCS3	GPIO_54	M3		
5	148	GP_CSD/ShutDown	-CS[3]	C3		SHDN		F3		GPMC_NCS4	GPIO_55	M2		
6	149	-WR	-WR	C8		NWR0/NWE		C9		GPMC_NWE		R3		
7	150	-RD/TIP	-RD	A8		NRD		F11		GPMC_NOE		R2		
8	151	-RST_IN	-PRST			-RST_IN				NRST_IN				
9	152	-RST_OUT	-RSTO			-RST_OUT				NRST_OUT			Buffer Reswarm	
10	153	-WAIT	-WAIT	A15		NWAIT	PC15	C6		GPMC_WAIT3	GPIO_65	U1		
11	154	-FLASH WP	NC			-FLASH WP				NFLASH_WP	GPIO_62	T2	Through resistor	
12	54	WAKEUP	GND			WKUP		C3		SYS_NIRQ	GPIO_0	Y1		
13	157	BOOT_OPTION0	BOOT0	R11		BMS		T11		SYS_BOOT[5:0]			See manual for modes	
14	158	BOOT_OPTION1	BOOT1	P11		FLASH DISABLE				SYS_BOOT[5:0]				
15	<b>System Bus</b>													
16	175	A0	A0	M2		A0		F13		GPMC_NADV_ALE		R1		
17	176	A1	A1	L3		A1		F14		GPMC_A1	GPIO_34	E3		
18	177	A2	A2	L2		A2		F18		GPMC_A2	GPIO_35	E2		
19	178	A3	A3	E4		A3		F15		GPMC_A3	GPIO_36	E1		
20	179	A4	A4	K1		A4		E14		GPMC_A4	GPIO_37	F7		
21	180	A5	A5	F5		A5		F17		GPMC_A5	GPIO_38	F6		
22	181	A6	A6	J1		A6		F16		GPMC_A6	GPIO_39	F4		
23	182	A7	A7	J3		A7		E17		GPMC_A7	GPIO_40	F3		
24	183	A8	A8	P9		A8		E15		GPMC_A8	GPIO_41	F2		
25	184	A9	A9	T8		A9		E16		GPMC_A9	GPIO_42	F1		
26	185	A10	A10	U8		A10		D18		GPMC_A10	GPIO_43	G6		
27	186	A11	A11	T7		A11		D17		CCDC_PCLK	GPIO_94	AD2	Video Capture	
28	187	A12	A12	P7		A12		C18		CCDC_FIELD	GPIO_95	AD1	3517 uses	
29	188	A13	A13	P6		A13		B18		CCDC_WEN	GPIO_98	AE3	muxed address bus	
30	189	A14	A14	R6		A14		A18		CCDC_DATA0	GPIO_99	AD4	so these lines	
31	190	A15	A15	P5		A15		B17		CCDC_DATA1	GPIO_100	AE4	were free for	
32	191	A16	A16/Boot Type	D9		A16		C10		CCDC_DATA2	GPIO_101	AC5	Video Capture	
33	192	A17	A17/Boot Type	B8		A17		B10		CCDC_DATA3	GPIO_102	AD5		
34	193	A18	A18/Boot Type	B6		A18		C17		CCDC_DATA4	GPIO_103	AE5		
35	194	A19	A19/Boot Type	C6		A19	PC2	B8		CCDC_DATA5	GPIO_104	Y6		
36	195	A20	A20/Debug	B4		A20	PC3	C8		CCDC_DATA6	GPIO_105	AB6		
37	196	A21	A21	E6		A23	PC6	D8		CCDC_DATA7	GPIO_106	AC6		
38	159	D0	D0	U9		D0		A17		GPMC_D0		G5		
39	160	D1	D1	R8		D1		D15		GPMC_D1		G4		
40	161	D2	D2	P8		D2		C15		GPMC_D2		G3		
41	162	D3	D3	U7		D3		B16		GPMC_D3		G2		
42	163	D4	D4	R7		D4		B15		GPMC_D4		G1		
43	164	D5	D5	N6		D5		D14		GPMC_D5		H2		
44	165	D6	D6	U6		D6		C14		GPMC_D6		H1		
45	166	D7	D7	T6		D7		A15		GPMC_D7		J5		
46	167	D8	D8	L4		D8		B14		GPMC_D8	GPIO_44	J4		
47	168	D9	D9	L1		D9		D13		GPMC_D9	GPIO_45	J3		
48	169	D10	D10	K3		D10		C13		GPMC_D10	GPIO_46	J2		
49	170	D11	D11	K4		D11		E13		GPMC_D11	GPIO_47	J1		
50	171	D12	D12	K2		D12		B13		GPMC_D12	GPIO_48	K4		
51	172	D13	D13	J4		D13		E12		GPMC_D13	GPIO_49	K3		
52	173	D14	D14	J2		D14		D12		GPMC_D14	GPIO_50	K2		
53	174	D15	D15	E5		D15		C12		GPMC_D15	GPIO_51	K1		
54	<b>POWER/GND</b>													
55	3	3.3VCC	3.3VCC			3.3VCC				3.3VCC				
56	4	3.3VCC	3.3VCC			3.3VCC				3.3VCC				
57	43	3.3VCC	3.3VCC			3.3VCC				3.3VCC				
58	44	3.3VCC	3.3VCC			3.3VCC				3.3VCC				
59	135	3.3VCC	3.3VCC			3.3VCC				3.3VCC				
60	136	3.3VCC	3.3VCC			3.3VCC				3.3VCC				
61	197	3.3VCC	3.3VCC			3.3VCC				3.3VCC				
62	198	3.3VCC	3.3VCC			3.3VCC				3.3VCC				
63	1	GND	GND			GND				GND				
64	2	GND	GND			GND				GND				
65	20	GND	GND			GND				GND				
66	21	GND	GND			GND				GND				
67	41	GND	GND			GND				GND				
68	42	GND	GND			GND				GND				
69	91	GND	GND			GND				GND				
70	92	GND	GND			GND				GND				
71	137	GND	GND			GND				GND				

69		138	GND	GND		GND				GND			
70		155	GND	GND		GND				GND			
71		156	GND	GND		GND				GND			
72		199	GND	GND		GND				GND			
73		200	GND	GND		GND				GND			
74		53	ANALOG_GND	ADCGND	F14	ADCGND		G2		AGND			
75		56	VSTBY	Vstandby_3.3		Vstandby_3.3				Vstandby_3.3			
76		55	AV_REF	ADCVCC	B17	TSADVREF		E2		NC			
77	JTAG	139	JTAG_TCK	JTAG_TCK	P10	JTAG_TCK		U10		JTAG_TCK		U25	
78		140	JTAG_TDI	JTAG_TDI	R10	JTAG_TDI		R10		JTAG_TDI		T23	
79		141	JTAG_TDO	JTAG_TDO	U10	JTAG_TDO		V10		JTAG_TDO		T24	
80		142	JTAG_TMS	JTAG_TMS	T10	JTAG_TMS		P10		JTAG_TMS		T22	
81		143	~JTAG_TRST	~JTAG_TRST	C15	JTAG_NTRST		N10		JTAG_NTRST		U24	
82		144	JTAG_RTCK	NC		JTAG_RTCK		R11		JTAG_RTCK		T21	
83	USB	5	Host_A+	USBP0	R17	HDP A		T17		HDP A1			From USB SW
84		7	Host_A-	USBM0	R15	HDMA		R17		HDMA1			From USB SW
85		6	Host_B+	USBP2	B15	HDPB		U15		HDP A2			From USB SW
86		8	Host_B-	USBM2	A16	HDMB		U16		HDMA2			From USB SW
87		9	Host/Device/OTG_C-	USBM1	U16	HDMB		U16		USB0_DM		F24	
88		11	Host/Device/OTG_C+	USBP1	T15	HDPB		U15		USB0_DP		F25	
89		10	USB_OTG_VBUS	NC		TWCK1/ISL_D11	PB11	M11	2ND I2C	USB0_VBUS		G24	
90		40	USB_OTG_ID	NC	M12	TWD1/ISI_D10	PB10	U13	2ND I2C	USB0_ID		G25	
91	ETH/MAGNETICS	12	Gig D-	NC		NC		NC		NC			
92		14	Gig D+	NC		NC		NC		NC			
93		13	Gig C-/Host/Device/OTG_C-	NC/USBM1	U16	pin will jumpered for SMI		NC		NC			
94		15	Gig C+	NC		NC		NC		NC			
95		16	Ethernet Rx-/Gig B-	Ethernet Rx-		Ethernet Rx-	From phy	Ethernet Rx-	From phy	Ethernet Rx-			From phy
96		18	Ethernet Rx+/Gig B+	Ethernet Rx+		Ethernet Rx+	From phy	Ethernet Rx+	From phy	Ethernet Rx+			From phy
97		17	Ethernet Tx-/Gig A-	Ethernet Tx-		Ethernet Tx-	From phy	Ethernet Tx-	From phy	Ethernet Tx-			From phy
98		19	Ethernet Tx+/Gig A+	Ethernet Tx+		Ethernet Tx+	From phy	Ethernet Tx+	From phy	Ethernet Tx+			From phy
99		38	LED_LINK/CFG_2	LED_LINK/CFG_2		ETH_LED1	From phy	ETH_LED1	From phy	ETH_LED1			From phy
100		39	LED_ACT/CFG_3	LED_ACT/CFG_3		ETH_LED2	From phy	ETH_LED2	From phy	ETH_LED2			From phy
101	SPI	22	SPI_MI	SSPRX	U13	SPI0_MISO		PB0	T4	MCSP1_SOMI	GPIO_173	AC15	
102		23	SPI_MO	SSPTX	R13	SPI0_MOSI		PB1	V2	MCSP1_SIMO	GPIO_172	AD15	
103		24	SPI_SCK	SCLK1	U12	SPI0_CLK		PB2	V3	MCSP1_CLK	GPIO_171	AE14	
104		25	SPI_CS0	EGPIO2/DMA_RQ	E13	TCLK1/SCK1		PD29	L10	MCSP1_CS0	GPIO_174	AB15	
105		26	SPI_CS1	EGPIO4/I2STD1	E14	SPI1_NPCS1/ADTRG		PD28	P9	MCSP1_CS1	GPIO_175	AD14	
106		27	SPI_CS2	EGPIO8/DMA_ACK1	H14	IRQ/SPI1_NPCS2		PD18	R9	MCSP1_CS2	GPIO_176	AE15	
107		28	SPI_CS3/SPI_Frame	SFRM1	T12	FIQ/SPI1_NPCS3		PD19	T9	MCSP1_CS3	GPIO_177	AE16	
108	I2C	29	I2CCLK	EECLK	N11	TWCK0		PA21	P4	I2C2_SCL	GPIO_168	W1	
109		30	I2CDATA	EEDAT	P12	TWD0		PA20	R2	I2C2_SDA	GPIO_183	W2	
110	SERIAL PORTS	95	COMA_TXD	TXD0	N15	Console TX		PB4	R5	UART1_TX	GPIO_148	AA19	
111		96	COMA_RXD	RXD0	N14	Console RX		PB5	V4	UART1_RX	GPIO_151	W20	
112		97	COMA_CTS	CTS0	N13			PD17	V9	UART1_CTS	GPIO_150	Y20	
113		98	COMA_RTS	RTS0	U15			PD16	N9	UART1_RTS	GPIO_149	Y19	
114		99	COMA_DTR	DTR0	R9			PD7	U7	MMC1_DAT4/DTR	GPIO_126	AB10	
115		100	COMA_DSR	DSR0/DCD0	T9	AC97FS/TIOB5		PD8	P8	MMC1_DAT6/DSR	GPIO_128	AD10	
116		101	COMA_RI	RI/EGPIO0	D15	AC97CK/TCLK5		PD9	R8	MMC1_DAT7/RI	GPIO_129	AE10	
117		102	COMB_TXD	TXD1	N16			PB13	R6	UART3_TX_IRTX	GPIO_166	P2	Console TX
118		103	COMB_RXD	RXD1	P16			PB12	P6	UART3_RX_IRRX	GPIO_165	P1	Console RX
119		104	COMB_CTS	FGPIO5	B13			PB26	U14	UART3_CTS_RCTX	GPIO_163	N2	
120		105	COMB_RTS	FGPIO0	A14			PB27	M13	UART3_RTS_SD	GPIO_164	N3	
121		106	COMC_TXD	TXD2	N17	RS485 Auto Port		PB6	T5	UART2_TX	GPIO_142	A24	
122		107	COMC_RXD	RXD2	P17			PB7	U5	UART2_RX	GPIO_143	C23	
123		108	COMC_CTS	GGPIO2	P15			PC11	B7	UART2_CTS	GPIO_140	B24	
124		109	COMC_RTS	FGPIO7	A13	RS485 EN		PC9	C7	UART2_RTS	GPIO_141	C24	
125		110	COMD_TXD	ROW4	M13			PB8	T12	UART4_TX	GPIO_63	T4	
126		111	COMD_RXD	ROW5	L14			PB9	N11	UART4_RX	GPIO_64	T5	
127		112	COMD_CTS	EGPIO1	D14			PD6	N8	UART4_CTS/CCDC_VD	GPIO_97	AD3	
128		113	COMD_RTS	EGPIO3/485EN	E15			PE0	G4	UART4_RTS/CCDC_HD	GPIO_96	AE2	
129	GPIO												
130	LCD Backlight On/Off	114	GPIO0	ROW0/GPIOC0	M15	LCDMOD		PE1	F4	GPT8_PWM_EVT	GPIO_146	E23	
131		115	GPIO1	ROW1/GPIOC1	M14	ISI_D0		PB20	P12	SYS_CLKOUT2	GPIO_186	M25	
132		116	GPIO2	ROW2/GPIOC2	M17	ISI_D1		PB21	T15	MCBSP4_SIMO	GPIO_158	P22	
133		117	GPIO3	ROW3/GPIOC3	M16	ISI_D2		PB22	R12	MCBSP4_SOMI	GPIO_159	P23	
134		118	GPIO4	COL0/GPIOD0	K17	ISI_D3		PB23	T16	HDQ_SIO	GPIO_170	L25	1-WIRE
135		119	GPIO5	COL1/GPIOD1	K16	ISI_D4		PB24	N12	I2C3_SCL	GPIO_184	W4	2ND I2C
136	USB_PWR_EN	120	GPIO6	COL2/GPIOD2	K15	ISI_D5		PB25	M12	I2C3_SDA	GPIO_185	W5	2ND I2C
137	SD_LED	121	GPIO7	COL3/GPIOD3	J17	TK1/PCK0		PD12	V8	MCBSP4_CLKX	GPIO_152	B23	2ND I2S
138	SD Power	122	GPIO8	ROW6/GPIO6	M12	RD1		PD11	T8	MCBSP4_DR	GPIO_153	A23	2ND I2S
139	SD Write Protect	123	GPIO9	COL7/DPIO7	H16	TD1		PD10	U8	MCBSP4_DX	GPIO_154	B22	2ND I2S
140		124	GPIO10	ROW7/GPIO7	L13	TF1/RF1		PD14/PD15	U9/M9	MCBSP4_FSX	GPIO_155	A22	2ND I2S
141		125	GPIO11	EGPIO6/I2STD2	F15	SPI1_MISO		PB14	M7	MCBSP2_CLK	GPIO_178	AD16	2ND SPI
142		126	GPIO12	EGPIO10/DREQ1	G14	SPI1_MOSI		PB15	V5	MCBSP2_SIMO	GPIO_179	AC16	2ND SPI
143		127	GPIO13	EGPIO11/DACK1	G15	SPI1_CLK		PB16	T6	MCBSP2_SOMI	GPIO_180	AB16	2ND SPI
144		128	GPIO14	PWMOUT/EGPIO14	R14	SPI1_NPCS0/RTS0		PB17	U6	MCBSP2_CS0	GPIO_181	AA16	2ND SPI
144		134	GPIO15	HGPIO7	D5	ISI_MCK/PCK1		PB31	P13	MCBSP2_CS1	GPIO_182	AE17	2ND SPI

145	<b>IRQs</b>	129	IRQA	INT0	T14		ISL_PCK	PB28	N13		UART1_CTS/MCBSP1_CLKS	GPIO_160	P25
146		130	IRQB	INT1	U14		ISL_VSYNC	PB29	R13		MCBSP14_CS0/MCBSP1_FS)	GPIO_161	P24
147		131	IRQC	INT2	T13		ISL_HSYNC	PB30	T13		MCBSP1_CLKX	GPIO_162	N24
148	<b>OSCILLATORS</b>	132	OSC0 (High)	8MHz		Prog. Osc.	TIOB0/SCK2	PD30	T10		GPT10_PWM_EVT	GPIO_145	F19
149		133	OSC1 (Low)	200KHz		Prog. Osc.	TIOB1/PWM1	PD31	L11		GPT09_PWM_EVT	GPIO_144	F20
150	<b>SD/MMC</b>	31	SDCLK	SCLK1	U12	No SD	MCI1_CK/PCK0	PA31	V1		MMC2_CLK	GPIO_130/GPIO_139	AA9
151		32	CMD	SSPTX	R13	Uses SPI	MCI1_CDA/SCK3	PA22	T1		MMC2_CMD	GPIO_131	AB9
152		33	DAT0	SSPRX	U13	instead	MCI1_DA0/RTS3	PA23	P5		MMC2_DAT0	GPIO_132	AC9
153		34	DAT1	EGPIO12/DMA_EOT1	D13		MCI1_DA1/CTS3	PA24	R3		MMC2_DAT1	GPIO_133	AD9
154		35	DAT2	EGPIO13/I2SRD2	C14		MCI1_DA2/PWM3	PA25	T2		MMC2_DAT2	GPIO_134	AE9
155		36	DAT3	COL6/DPIO6	J15		MCI1_DA3/TIOB2	PA26	T3		MMC2_DAT3	GPIO_135	AA10
156		37	Card_Detect	EGPIO15	B14		MCI1_DA4	PA27	U1		MMC1_DAT5	GPIO_127	AC10
157	<b>CAN</b>	93	CANTX	COL4/DPIO4	K13	No CAN	TXD0	PB19	P7	No CAN	HECC1_TXD	GPIO_130	V2
158		94	CANRX	COL5/DPIO5	J14		RXD0	PB18	N7		HECC1_RXD	GPIO_131	V3
159	<b>LCD</b>	57	LCD_BLUE0	P0	R5		LCDD23	PE23	K2		DSS_DOUT0	GPIO_70	AD24
160		58	LCD_BLUE1	P0	R5		LCDD24	PE24	K5		DSS_DOUT1	GPIO_71	AD25
161		59	LCD_BLUE2	P0	R5		LCDD25	PE25	K6		DSS_DOUT2	GPIO_72	AC23
162		60	LCD_BLUE3	P1	U5		LCDD26	PE26	K3		DSS_DOUT3	GPIO_73	AC24
163		61	LCD_BLUE4	P2	T5		LCDD27	PE27	K7		DSS_DOUT4	GPIO_74	AC25
164		62	LCD_BLUE5	P3	P4		LCDD28	PE28	K8		DSS_DOUT5	GPIO_75	AB24
165		63	LCD_BLUE6	P4	U4		LCDD29	PE29	L3		DSS_DOUT6	GPIO_76	AB25
166		64	LCD_BLUE7	P5	R4		LCDD30	PE30	L2		DSS_DOUT7	GPIO_77	AA23
167		65	LCD_GREEN0	P6	R3		LCDD15	PE15	J4		DSS_DOUT8	GPIO_78	AA24
168		66	LCD_GREEN1	P6	R3		LCDD16	PE16	J2		DSS_DOUT9	GPIO_79	AA25
169		67	LCD_GREEN2	P6	R3		LCDD17	PE17	J6		DSS_DOUT10	GPIO_80	Y22
170		68	LCD_GREEN3	P7	T4		LCDD18	PE18	J7		DSS_DOUT11	GPIO_81	Y23
171		69	LCD_GREEN4	P8	U3		LCDD19	PE19	J1		DSS_DOUT12	GPIO_82	Y24
172		70	LCD_GREEN5	P9	R1		LCDD20	PE20	J8		DSS_DOUT13	GPIO_83	Y25
173		71	LCD_GREEN6	P10	P2		LCDD21	PE21	K1		DSS_DOUT14	GPIO_84	W21
174		72	LCD_GREEN7	P11	P3		LCDD22	PE22	K4		DSS_DOUT15	GPIO_85	W22
175		73	LCD_RED0	P12	N5		LCDD0	PE7	H6		DSS_DOUT16	GPIO_86	W23
176		74	LCD_RED1	P12	N5		LCDD1	PE8	G6		DSS_DOUT17	GPIO_87	W24
177		75	LCD_RED2	P12	N5		LCDD2	PE9	H7		DSS_DOUT18	GPIO_88	W25
178		76	LCD_RED3	P13	N4		LCDD3	PE10	H8		DSS_DOUT19	GPIO_89	V24
179		77	LCD_RED4	P14	N1		LCDD4	PE11	G8		DSS_DOUT20	GPIO_90	V25
180		78	LCD_RED5	P15	N3		LCDD5	PE12	J5		DSS_DOUT21	GPIO_91	U21
181		79	LCD_RED6	P16	N2		LCDD6	PE13	H4		DSS_DOUT22	GPIO_92	U22
182		80	LCD_RED7	P17	M5		LCDD7	PE14	J3		DSS_DOUT23	GPIO_93	U23
183		81	LCD_HORZ/LP	HSYNC	R2		LCDHSYNC	PE4	G7		DSS_HSYNC	GPIO_67	AD22
184		82	LCD_VERT/FP/FLM	VSYNC	T3		LCDVSYNC	PE3	F5		DSS_VSYNC	GPIO_68	AD23
185		83	LCD_ENABLE/DE/M	BLANK	L5		LCDDEN	PE6	G3		DSS_ACBIAS	GPIO_69	AE24
186		84	LCD_CLK/SFK/SFCLK	SPCLK	P1		LCDDOTCK	PE5	H5		DSS_PCLK	GPIO_66	AE23
187		85	BCKLIGHT/PWM	BRIGHT/GPIOM7	M1		LCDDC	PE2	G5		GPT11_PWM_EVT	GPIO_146	E24
188	<b>TOUCH/ADC</b>	45	X+/Xr/ADC0	XP/ADC0	C16		TSAD0/XP	PD20	D2		TS_XP		External Touch Controller
189		46	X-/Xi/ADC1	XM/ADC1	C17		TSAD1/XM	PD22	F1		TS_XM		
190		47	Y+/Yu/ADC2	YP/ADC2	D17		TSAD2/YP	PD21	E1		TS_YP		
191		48	Y-/Yd/ADC3	YM/ADC3	D16		TSAD3/YM	PD23	G2		TS_YM		
192		49	SX+/ADC4	SXP/ADC4	E16		GPAD4/SPIO_CS1/PWM0	PD24	F2	Was shared with SoM Pin 25	ADC4		
193		50	SX-/ADC5	SXM/ADC5	E17		GPAD5/SPIO_CS2/PWM1	PD25	G1				
194		51	SY+/ADC6	SYP/ADC6	F17		GPAD6/PCK0/PWM2	PD26	H1				
195		52	SY-/ADC7	SYM/ADC7	F16		GPAD7/SPIO_CS3/PCK1	PD27	H2				
196	<b>AUDIO</b>	86	AudioA_SCLK	ABITCLK	R16		TK0/PWM3	PD0	R7		MCBSP2_CLKX	GPIO_117	C25
197		87	AudioA_LRCLK/Frame	ASYNCR	R12		TF0 & RF0	PD1/PD5	T7/V7		MCBSP2_FSX	GPIO_116	D25
198		88	AudioA_MCLK	ARST	B16		12.288 MHz Clock				12.288 MHz Clock		
199		89	AudioA_DIN	ASDI	A17		TD0	PD2	L8		MCBSP2_DR	GPIO_118	B25
200		90	AudioA_DOUT	ASDO	N12		RD0	PD3	V6		MCBSP2_DX	GPIO_119	D24
<b>UNUSED PINS</b>													
				GPIOH2	A7		DQS0		A11				
				GPIOH3	B7		DQS1		E11				
				GPIOH4	D8		JTAGSEL		E4	Tied Low			
				GPIOH5	C7		NSDCK		A14				
				GPIOH6	D6		NWR1/NBS1		D9				
							NWR3/NBS3		A9				
							PC0		A8				
							PC7		A6				
							PC12		A5				
							PD13		L9				