

SoM-200 Pin Specification

Revision 3.0 4/25/2012

Revision Log:

8/20/2009 New specification for 200 pin SoM to include Gig Ethernet & USB power control.
 12/22/2010 Switched SPI Lines on SoM-9G45/9M10
 1/10/2010 Substituted NCS2 for NCS5 on SoM-9G45/9M10
 4/25/2012 Added SoM-3517 updated pin-out

	PIN#	TI AM3354	Port Line	BGA Pin#	Comment	SAMA5036	Port Line	BGA Pin#	Comment	IMXRT1062	Port Line	BGA Pin#	Comment
1		System Control											
2	145	GPIC_CSn0	GPIO_1_29	V6		NCS1/TIOA2	PE27	M12		SEMC_CSX00	GPIO_EMC_41	C7	
3	146	GPIC_CSn4	GPIO_0_30	T17		NCS2/TIOB2	PE28	L13		SEMC_CSX03	GPIO_B0_02	E8	SHARED WITH LCDFIF_HSYNC
4	147	GPIC_CSn5	GPIO_0_31	U17		NCS3/NANDCS		L12		NC			NC TO PROCESSOR
5	148	GPIC_CSn6	GPIO_1_28	U18		SHDN#		T12	EXTERNAL POWER ENABLE	ONOFF		M6	10k PULL UP TO 3.3V
6	149	GPIC_WEn	GPIO_2_4	U6		NWE		K11		SEMC_ADDR11	GPIO_EMC_19	B4	BUFFER OF SEMC_ADDR_11
7	150	GPIC_OEn	GPIO_2_3	T7		NRD		L17		SEMC_ADDR11	GPIO_EMC_19	B4	BUFFER OF SEMC_WE#
8	151	WARMRSTn		A10		NRST		V9		RST_IN#			INPUT TO RESET SUP
9	152	WARMRSTn		A10		NRST		V9				M7	
10	153	GPIC_WAIT1	GPIO_2_1	V12		NWAIT	PE30	L14		POR#			NC TO PROCESSOR
11	154	NC				NC				NC			NC TO PROCESSOR
12	157	SYS_BOOT[5:0]			SPI flash disable	BMS		T10		WAKEUP		L6	
13	158	NC				NC		U9		SRC_BOOT_MODE0	GPIO_AD_B0_04	F11	SHARED WITH LPSPI3_PCS1#
14		System Bus				NC				SRC_BOOT_MODE1	GPIO_AD_B0_05	G14	SHARED WITH LPSPI3_PCS2#
15	175	GPIC_A0	GPIO_1_16	R13		ADNSB0	PE0	P13		SEMC_DATA00	GPIO_EMC_00	E3	BUFFERED THROUGH LATCH
16	176	GPIC_A1	GPIO_1_17	V14		A1	PE1	R14		SEMC_DATA01	GPIO_EMC_01	F3	BUFFERED THROUGH LATCH
17	177	GPIC_A2	GPIO_1_18	U14		A2	PE2	R13		SEMC_DATA02	GPIO_EMC_02	F4	BUFFERED THROUGH LATCH
18	178	GPIC_A3	GPIO_1_19	T14		A3	PE3	V18		SEMC_DATA03	GPIO_EMC_03	G4	BUFFERED THROUGH LATCH
19	179	GPIC_A4	GPIO_1_20	R14		A4	PE4	P14		SEMC_DATA04	GPIO_EMC_04	F2	BUFFERED THROUGH LATCH
20	180	GPIC_A5	GPIO_1_21	V15		A5	PE5	U18		SEMC_DATA05	GPIO_EMC_05	G5	BUFFERED THROUGH LATCH
21	181	GPIC_A6	GPIO_1_22	U15		A6	PE6	R18		SEMC_DATA06	GPIO_EMC_06	H5	BUFFERED THROUGH LATCH
22	182	GPIC_A7	GPIO_1_23	T15		A7	PE7	R15		SEMC_DATA07	GPIO_EMC_07	H4	BUFFERED THROUGH LATCH
23	183	GPIC_A8	GPIO_1_24	V16		A8	PE8	P17		SEMC_DATA08	GPIO_EMC_08	C6	BUFFERED THROUGH LATCH
24	184	GPIC_A9	GPIO_1_25	U16		A9	PE9	P15		SEMC_DATA09	GPIO_EMC_31	C5	BUFFERED THROUGH LATCH
25	185	GPIC_A10	GPIO_1_26	T16		A10	PE10	P18		SEMC_DATA10	GPIO_EMC_32	D5	BUFFERED THROUGH LATCH
26	186	GPIC_A11	GPIO_1_27	V17		A11	PE11	R16		SEMC_DATA11	GPIO_EMC_33	C4	BUFFERED THROUGH LATCH
27	187	NC				A12	PE12	N16		SEMC_DATA12	GPIO_EMC_34	D4	BUFFERED THROUGH LATCH
28	188	NC				A13	PE13	R17		SEMC_DATA13	GPIO_EMC_35	E5	BUFFERED THROUGH LATCH
29	189	NC				A14	PE14	N17		SEMC_DATA14	GPIO_EMC_36	C3	BUFFERED THROUGH LATCH
30	190	NC				A15	PE15	R18		SEMC_DATA15	GPIO_EMC_37	E4	BUFFERED THROUGH LATCH
31	191	NC				A16	PE16	N18		NC			NC TO PROCESSOR
32	192	NC				A17	PE17	P16		NC			NC TO PROCESSOR
33	193	NC				A18	PE18	M18		NC			NC TO PROCESSOR
34	194	NC				A19	PE19	N15		NC			NC TO PROCESSOR
35	195	NC				A20	PE20	M15		NC			NC TO PROCESSOR
36	196	NC				A21	PE21	N14		NC			NC TO PROCESSOR
37	159	GPIC_AD0	GPIO_1_0	U7		D0		K12		SEMC_DATA00	GPIO_EMC_00	E3	BUFFERED THROUGH LATCH
38	160	GPIC_AD1	GPIO_1_1	V7		D1		K15		SEMC_DATA01	GPIO_EMC_01	F3	BUFFERED THROUGH LATCH
39	161	GPIC_AD2	GPIO_1_2	R8		D2		K14		SEMC_DATA02	GPIO_EMC_02	F4	BUFFERED THROUGH LATCH
40	162	GPIC_AD3	GPIO_1_3	T8		D3		K16		SEMC_DATA03	GPIO_EMC_03	G4	BUFFERED THROUGH LATCH
41	163	GPIC_AD4	GPIO_1_4	U8		D4		K13		SEMC_DATA04	GPIO_EMC_04	F2	BUFFERED THROUGH LATCH
42	164	GPIC_AD5	GPIO_1_5	V8		D5		K17		SEMC_DATA05	GPIO_EMC_05	G5	BUFFERED THROUGH LATCH
43	165	GPIC_AD6	GPIO_1_6	R9		D6		J12		SEMC_DATA06	GPIO_EMC_06	H5	BUFFERED THROUGH LATCH
44	166	GPIC_AD7	GPIO_1_7	T9		D7		K18		SEMC_DATA07	GPIO_EMC_07	H4	BUFFERED THROUGH LATCH
45	167	NC				D8		J14		SEMC_DATA08	GPIO_EMC_30	C6	BUFFERED THROUGH LATCH
46	168	NC				D9		J16		SEMC_DATA09	GPIO_EMC_31	C5	BUFFERED THROUGH LATCH
47	169	NC				D10		J13		SEMC_DATA10	GPIO_EMC_32	D5	BUFFERED THROUGH LATCH
48	170	NC				D11		J17		SEMC_DATA11	GPIO_EMC_33	C4	BUFFERED THROUGH LATCH
49	171	NC				D12		J15		SEMC_DATA12	GPIO_EMC_34	D4	BUFFERED THROUGH LATCH
50	172	NC				D13		J18		SEMC_DATA13	GPIO_EMC_35	E5	BUFFERED THROUGH LATCH
51	173	NC				D14		H16		SEMC_DATA14	GPIO_EMC_36	C3	BUFFERED THROUGH LATCH
52	174	NC				D15		H18		SEMC_DATA15	GPIO_EMC_37	E4	BUFFERED THROUGH LATCH
53	3	3.3VCC				3.3VCC				3.3VCC			
54	4	3.3VCC				3.3VCC				3.3VCC			
55	43	3.3VCC				3.3VCC				3.3VCC			
56	44	3.3VCC				3.3VCC				3.3VCC			
57	135	3.3VCC				3.3VCC				3.3VCC			
58	136	3.3VCC				3.3VCC				3.3VCC			
59	137	3.3VCC				3.3VCC				3.3VCC			
60	138	3.3VCC				3.3VCC				3.3VCC			
61	139	3.3VCC				3.3VCC				3.3VCC			
62	1	GND				GND				GND			
63	2	GND				GND				GND			
64	20	GND				GND				GND			
65	21	GND				GND				GND			
66	41	GND				GND				GND			
67	42	GND				GND				GND			
68	91	GND				GND				GND			
69	92	GND				GND				GND			
70	137	GND				GND				GND			
71	138	GND				GND				GND			
72	155	GND				GND				GND			
73	156	GND				GND				GND			
74	159	GND				GND				GND			
75	200	GND				GND				GND			
76	53	AGND				ANALOG_GND				GND			
77	56	Vstandby_3.3				VSTBY				3V0_BKUP			
78	55	NC				NC				ADC_VCC			
79	139	MPU_JTAG_TCK		A12		TCK/SWCLK		P9		JTAG_TCK	GPIO_AD_B0_07	F12	SHARED WITH ENET_CRS_DV
80	140	MPU_JTAG_TDI		B11		TDI		R8		JTAG_TDI	GPIO_AD_B0_09	F14	SHARED WITH ENET_IRQ
81	141	MPU_JTAG_TDO		A11		TDO		M11		JTAG_TDO	GPIO_AD_B0_10	G13	SHARED WITH GPIO_IRQ
82	142	MPU_JTAG_TMS		C11		TMS/SWDIO		N10		JTAG_TMS	GPIO_AD_B0_06	E14	SHARED WITH LPSPI3_PCS3#
83	143	MPU_JTAG_TRSTn		B10		NTRST		P11		JTAG_TRSTB	GPIO_AD_B0_11	G10	SHARED WITH USB_OTG1_ID
84	144	NC				NC				NC			
85	5	HDPA1			From USB SW	HSDPB		U12		USB_OTG2_D_P		P7	THROUGH USB HUB

84		7	HDMA1			From USB SW	HHSMB	V12	USB_OTG2_D_N	N7	THROUGH USB HUB		
85		6	HDP2			From USB SW	HHSDP2	U14	USB_OTG2_D_P	P7	THROUGH USB HUB		
86		8	HDMA2			From USB SW	HHSDMC	U14	USB_OTG2_D_N	N7	THROUGH USB HUB		
87		9	USB0_DM	N18			H/HSDMA	V10	USB_OTG1_DN	M8			
88		11	USB0_DP	M17			H/HSDPA	U10	USB_OTG1_DP	L8			
89		10	USB0_VBUS	P15			USB_VBUS	PC27	USB_OTG1_VBUS_FING		INPUT POWER FOR OTG		
90		40	USB0_ID	P16			USB_ID	PC26	GPIO1_IO11	GPIO_AD_B0_11	G10	SHARED WITH TRSTB	
91	ETH/MAGNETICS	12	NC				TXRXM_D		From phy	NC	NC TO PROCESSOR		
92		14	NC				TXRX_P_D		From phy	NC	NC TO PROCESSOR		
93		13	NC				TXRXM_C		From phy	NC	NC TO PROCESSOR		
94		15	NC				TXRX_P_C		From phy	NC	NC TO PROCESSOR		
95		16	Ethernet Rx-			From phy	TXRXM_B		From phy	MDI_RX_N	FROM PHY		
96		18	Ethernet Rx+			From phy	TXRX_P_B		From phy	MDI_RX_P	FROM PHY		
97		17	Ethernet Tx-			From phy	TXRXM_A		From phy	MDI_TX_N	FROM PHY		
98		19	Ethernet Tx+			From phy	TXRX_P_A		From phy	MDI_TX_P	FROM PHY		
99		38	ETH_LED1			From phy	LED2/PHYAD1		From phy	ETH_LINK#	FROM PHY		
100		39	ETH_LED2			From phy	LED1/PHYAD0		From phy	ETH_ACT#	FROM PHY		
101	SPI	22	SPI0_D0	GPIO_0_3	B17		SPI0_MISO	PD10	K9	LPSP3_SIN	GPIO_AD_B0_02	M11	
102		23	SPI0_D1	GPIO_0_4	B16		SPI0_MOSI	PD11	M5	LPSP3_SOUT	GPIO_AD_B0_01	H10	
103		24	SPI0_SCLK	GPIO_0_2	A17		SPI0_SPCK	PD12	K10	LPSP3_SCK	GPIO_AD_B0_00	M14	
104		25	GPIO_1_15	GPIO_1_15	U13		SPI0_NPCS1	PD14	L9	LPSP3_PCS0	GPIO_AD_B0_03	G11	
105		26	GPIO_3_8	GPIO_3_8	B14		SPI0_NPCS2	PD15	N3	LPSP3_PCS1	GPIO_AD_B0_04	F11	SHARED WITH BOOT_0
106		27	GPIO_1_14	GPIO_1_14	V13		SPI0_NPCS3	PD16	L10	LPSP3_PCS2	GPIO_AD_B0_05	G14	SHARED WITH BOOT_1
107		28	GPIO_3_18	GPIO_3_18	B12		SPI0_NPCS4	PC28	H8	LPSP3_PCS3	GPIO_AD_B0_06	E14	SHARED WITH TMS (NC NORM)
108	I2C	29	I2C0_SCL	GPIO_3_6	C16		TWCK0/I2XD1	PA31	K3	LP12C1_SCL	GPIO_SD_B1_04	P2	10k PULL UP TO 3.3V
109		30	I2C0_SDA	GPIO_3_5	C17		TWDD0/I2XD1	PA30	H1	LP12C1_SDA	GPIO_SD_B1_05	N3	10k PULL UP TO 3.3V
110	SERIAL PORTS	95	EUART0_TX			From ext UART	TXD1	PB29	P8	LPUART2_TXD	GPIO_AD_B1_02	L11	
111		96	EUART0_RX			From ext UART	RXD1	PB28	M9	LPUART2_RXD	GPIO_AD_B1_03	M12	
112		97	EUART0_CTS			From ext UART	CTS1	PB26	N9	LPUART2_CTS_B	GPIO_AD_B1_00	J11	
113		98	EUART0_RTS			From ext UART	RTS1	PB27	V4	LPUART2_RTS_B	GPIO_AD_B1_01	K11	
114		99	EUART0_DTR			From ext UART	TIOA3	PC0	D8	NC			NC TO PROCESSOR
115		100	EUART0_DSR			From ext UART	TIOB3	PC1	A4	NC			NC TO PROCESSOR
116		101	EUART0_RI			From ext UART	TCLK3	PC2	E8	NC			NC TO PROCESSOR
117		102	UART0_TX	GPIO_1_11	E16		DTXD	PB31	R9	LPUART1_TXD	GPIO_AD_B0_12	K14	
118		103	UART0_RX	GPIO_1_10	E15		DRXD	PB30	M10	LPUART1_RXD	GPIO_AD_B0_13	L14	
119		104	NC				TIOA4	PC3	A3	NC			NC TO PROCESSOR
120		105	NC				TIOB4	PC4	A2	NC			NC TO PROCESSOR
121		106	UART1_TX	GPIO_0_15	D15		TXD2	PE26	M14	LPUART4_TXD	GPIO_SD_B1_00	L5	
122		107	UART1_RX	GPIO_0_14	D16		RXD2	PE25	N12	LPUART4_RXD	GPIO_SD_B1_01	M5	
123		108	UART1_CTS	GPIO_0_12	D18		CTS2	PE23	M13	NC			NC TO PROCESSOR
124		109	UART1_RTS	GPIO_0_13	D17		RTS2	PE24	M16	NC			NC TO PROCESSOR
125		110	EUART1_TX			From ext UART	TXD0	PD18	M6	UART5_TXD	GPIO_B1_12	D13	SHARED WITH BT_TX
126		111	EUART1_RX			From ext UART	RXD0	PD17	N5	UART5_RXD	GPIO_B1_13	D14	SHARED WITH BT_RX
127		112	EUART1_CTS			From ext UART	CTS0	PC29	G5	NC			NC TO PROCESSOR
128		113	EUART1_RTS			From ext UART	RTS0	PC30	D3	NC			NC TO PROCESSOR
129	LCD	114	GPIO_1_13	GPIO_1_13	R12		PWMH2/TIOA0	PD5	P2	GPA_0			FROM GPIO EXPANDER
130	Backlight On/Off	115	EGPIO0			From ext GPIO	PWMH3/TIOA0	PD7	R2	GPA_1			FROM GPIO EXPANDER
131		116	EGPIO1			From ext GPIO	EREXER/TIOA5	PC5	B3	GPA_2			FROM GPIO EXPANDER
132		117	EGPIO2			From ext GPIO	EREFCK/TIOB5	PC7	G8	GPA_3			FROM GPIO EXPANDER
133		118	EGPIO3			From ext GPIO	EMDC/TCLK5	PC8	B4	GPA_4			FROM GPIO EXPANDER
134		119	EGPIO4			From ext GPIO	EMDIO	PC9	F7	GPA_5			FROM GPIO EXPANDER
135		120	EGPIO5			From ext GPIO	MC12_CDA	PC10	A1	GPA_6			FROM GPIO EXPANDER
136	USB_PWR_EN	121	EGPIO6			From ext GPIO	MC12_DA0	PC11	D7	GPA_7			FROM GPIO EXPANDER
137	SD_LED	122	EGPIO7			From ext GPIO	MC12_DA1/TIOA1	PC12	G6	GPB_0			FROM GPIO EXPANDER
138	SD Power	123	GPIO_3_7	GPIO_3_7	C14		MC12_DA2/TIOB1	PC13	E7	GPB_1			FROM GPIO EXPANDER
139	SD Write Protect	124	GPIO_0_18	GPIO_0_18	F16		MC12_DA3/TCLK1	PC14	B2	GPB_2			FROM GPIO EXPANDER
140		125	SPI1_D0	GPIO_1_9	E18		MC12_CK/PCK2	PC15	F6	GPB_3			FROM GPIO EXPANDER
141		126	SPI1_D1	GPIO_1_9	E17		SPI1_MSIO	PC22	C2	GPB_4			FROM GPIO EXPANDER
142		127	SPI1_SCLK	GPIO_0_7	C18		SPI1_MOSI	PC23	G9	GPB_5			FROM GPIO EXPANDER
143		128	TIMER4	GPIO_2_2	R7	PWM	SPI1_SPCK	PC24	C1	GPB_6			GPIO EXP/SHARED WITH BT
144		134	INT1	GPIO_0_20	D14		SPI1_NPCS0	PC25	H10	GPB_7			GPIO EXP/SHARED WITH BT
145	IRQs	129	INT0	GPIO_0_19	A15		FIQ/PWMF1	PC31	E4	GPIO1_IO21	GPIO_AD_B1_05	K12	SHARED WITH BT
146		130	GPIO_3_19	GPIO_3_19	C13		IRQ/PWML1	PE31	L16	PMIC_STBY_REQ	GPIO5_IO02	L7	
147		131	GPIO_3_21	GPIO_3_21	A14		TCLK2	PE29	L15	PMIC_ON_REQ	GPIO5_IO02	K7	
148	OSCILLATORS	132	OSC0			From ext CLK	PCK0/AD10	PD30	J1	CCM_CLK1_P		N13	
149		133	TIMERS	GPIO_2_5	T6		PCK1/AD11	PD31	J2	CCM_CLK1_N		P13	
150	SD/MMC	31	MMC1_CLK	GPIO_1_30	U8		MC11_CK	PB24	M8	SDIO_CLK			FROM USB HUB
151		32	MMC1_CMD	GPIO_1_31	V9		MC11_CDA	PB19	T6	SDIO_CMD			FROM USB HUB
152		33	MMC1_DATA0	GPIO_0_22	U10		MC11_DA0	PB20	N8	SDIO_D0			FROM USB HUB
153		34	MMC1_DAT1	GPIO_0_23	T10		MC11_DA1	PB21	U4	SDIO_D1			FROM USB HUB
154		35	MMC1_DAT2	GPIO_0_26	T11		MC11_DA2	PB22	M7	SDIO_D2			FROM USB HUB
155		36	MMC1_DAT3	GPIO_0_27	U12		MC11_DA3	PB23	U5	SDIO_D3			FROM USB HUB
156		37	GPMC_AD12	GPIO_1_12	T12		ECTSDV/TCLK4	PC5	F8	SDIO_CD			FROM USB HUB
157	CAN	93	MII1_TXD3	GPIO_0_16	J18		CANTX1	PB15	U3	FLEXCAN1_TX	GPIO_SD_B1_02	M3	
158		94	MII1_TXD2	GPIO_0_17	K15		CANRX1	PB14	R7	FLEXCAN1_RX	GPIO_SD_B1_03	M4	
159	LCD	57	LCD_DATA0	GPIO_2_6	R1		LCDATA0	PA0	E3	LCD_DATA00	GPIO_B0_04	C8	LSB REPETED 5-6-5 FORMAT
160		58	LCD_DATA1	GPIO_2_6	R1		LCDATA1	PA1	F5	LCD_DATA00	GPIO_B0_04	C8	LSB REPETED 5-6-5 FORMAT
161		59	LCD_DATA2	GPIO_2_6	R1		LCDATA2	PA2	D2	LCD_DATA00	GPIO_B0_04	C8	LSB REPETED 5-6-5 FORMAT
162		60	LCD_DATA3	GPIO_2_6	R1		LCDATA3	PA3	F4	LCD_DATA00	GPIO_B0_04	C8	LSB REPETED 5-6-5 FORMAT
163		61	LCD_DATA4	GPIO_2_7	R2		LCDATA4	PA4	D1	LCD_DATA01	GPIO_B0_05	B8	
164		62	LCD_DATA5	GPIO_2_8	R3		LCDATA5	PA5	J10	LCD_DATA02	GPIO_B0_06	A8	LCDIF. DATA_00-11
165		63	LCD_DATA6	GPIO_2_9	R4		LCDATA6	PA6	G4	LCD_DATA03	GPIO_B0_07	A9	USED FOR BOOT STRAP
166		64	LCD_DATA7	GPIO_2_10	T1		LCDATA7	PA7	J9	LCD_DATA04	GPIO_B0_08	B9	
167		65	LCD_DATA8	GPIO_2_11	T2		LCDATA8	PA8	F3	LCD_DATA05	GPIO_B0_09	C9	LSB REPETED 5-6-5 FORMAT
168		66	LCD_DATA9	GPIO_2_11	T2		LCDATA9	PA9	J8	LCD_DATA05	GPIO_B0_09	C9	LSB REPETED 5-6-5 FORMAT
169		67	LCD_DATA5	GPIO_2_11	T2		LCDATA10	PA10	E2	LCD_DATA05	GPIO_B0_09	C9	LSB REPETED 5-6-5 FORMAT
170		68	LCD_DATA6	GPIO_2_12	T3		LCDATA11	PA11	K8	LCD_DATA06	GPIO_B0_10	D9	
171		69	LCD_DATA7	GPIO_2_13	T4		LCDATA12	PA12	F2	LCD_DATA07	GPIO_B0_11	A10	
172		70	LCD_DATA8	GPIO_2_14	U1		LCDATA13	PA13	G6	LCD_DATA08	GPIO_B0_12	C10	
173		71	LCD_DATA9	GPIO_2_15	U2		LCDATA14	PA14	E1	LCD_DATA09	GPIO_B0_13	D10	
174		72	LCD_DATA10	GPIO_2_16	U3		LCDATA15	PA15	H1	LCD_DATA10	GPIO_B0_14	E10	

175	73	LCD_DATA11	GPIO_2_17	U4	LCDDATA16	PA16	H3	LCD_DATA11	GPIO_B0_15	E11	LSB REPETED 5-6-5 FORMAT
176	74	LCD_DATA11	GPIO_2_17	U4	LCDDATA17	PA17	H6	LCD_DATA11	GPIO_B0_15	E11	LSB REPETED 5-6-5 FORMAT
177	75	LCD_DATA11	GPIO_2_17	U4	LCDDATA18	PA18	H4	LCD_DATA11	GPIO_B0_15	E11	LSB REPETED 5-6-5 FORMAT
178	76	LCD_DATA11	GPIO_2_17	U4	LCDDATA19	PA19	H7	LCD_DATA11	GPIO_B0_15	E11	LSB REPETED 5-6-5 FORMAT
179	77	LCD_DATA12	GPIO_0_8	V2	LCDDATA20	PA20	H2	LCD_DATA12	GPIO_B1_00	A11	
180	78	LCD_DATA13	GPIO_0_9	V3	LCDDATA21	PA21	J6	LCD_DATA13	GPIO_B1_01	B11	
181	79	LCD_DATA14	GPIO_0_10	V4	LCDDATA22	PA22	G2	LCD_DATA14	GPIO_B1_02	C11	
182	80	LCD_DATA15	GPIO_0_11	T5	LCDDATA24	PA23	J5	LCD_DATA15	GPIO_B1_03	D11	
183	81	LCD_HSYNC	GPIO_2_23	R5	LCDSYNC	PA27	J3	LCD_HSYNC	GPIO_B0_02	E8	SHARED WITH SEMC_CSX3
184	82	LCD_VSYNC	GPIO_2_22	U5	LCDSYNC	PA26	G3	LCD_VSYNC	GPIO_B0_03	D8	
185	83	LCD_ACBIAS_EN	GPIO_2_25	R6	LCDDEN	PA29	K4	LCD_ENABLE	GPIO_B0_01	E7	
186	84	LCD_PCLK	GPIO_2_24	V5	LCDPCK	PA28	G1	LCD_CLK	GPIO_B0_00	D7	
187	85	MCASP0_AHCLKR	GPIO_3_17	C12	LCDPWM	PA24	F1	GPIO1_IQ24	GPIO_AD_B1_08	H13	
188	TOUCH/ADC										
189	46	AIN0		B6	AD0	PD20	N2	TCH_X_P			TO TROUGH SCREEN CONT.
190	47	AIN1		C7	AD1	PD21	M3	TCH_X_N			TO TROUGH SCREEN CONT.
191	48	AIN2		B7	AD2	PD22	M2	TCH_Y_P			TO TROUGH SCREEN CONT.
192	49	AIN3		A7	AD3	PD23	L3	TCH_Y_N			TO TROUGH SCREEN CONT.
193	50	AIN4		C8	AD4	PD24	M1	ADC_IN_0			TO EXT. ADC
194	51	AIN5		B8	AD5	PD25	N1	ADC_IN_1			TO EXT. ADC
195	52	AIN6		A8	AD6	PD26	L1	ADC_IN_2			TO EXT. ADC
196	86	MCASP0_ACLKX	GPIO_3_14	A13	TK0	PC16	B1	SAI1_TX_BCLK	GPIO_AD_B1_14	G12	SHARED WITH BT
197	87	MCASP0_FSX	GPIO_3_15	B13	TF0	PC17	E6	SAI1_TX_SYNC	GPIO_AD_B1_15	J14	SHARED WITH BT
198	88	12.288 MHz Clock			12.288MHz CLK			SAI1_MCLK	GPIO_AD_B1_09	M13	
199	89	MCASP0_AXR0	GPIO_3_16	D12	RD0	PC21	D5	SAI1_RX_DATA0	GPIO_AD_B1_12	H12	SHARED WITH BT
200	90	MCASP0_AXR1	GPIO_3_20	D13	TD0	PC18	C3	SAI1_TX_DATA0	GPIO_AD_B1_13	H11	SHARED WITH BT
UNUSED PINS											
					JTAGSEL		T9	INTERNAL PULL DOWN			
					LCDDISP	PA25	J4				
					DIBP		V6				
					DIBN		U6				
					NANDRDY		L18				
					AD8	PD28	K1				
					RF0	PC20	C4				
					TST		U15	PULLED LOW			