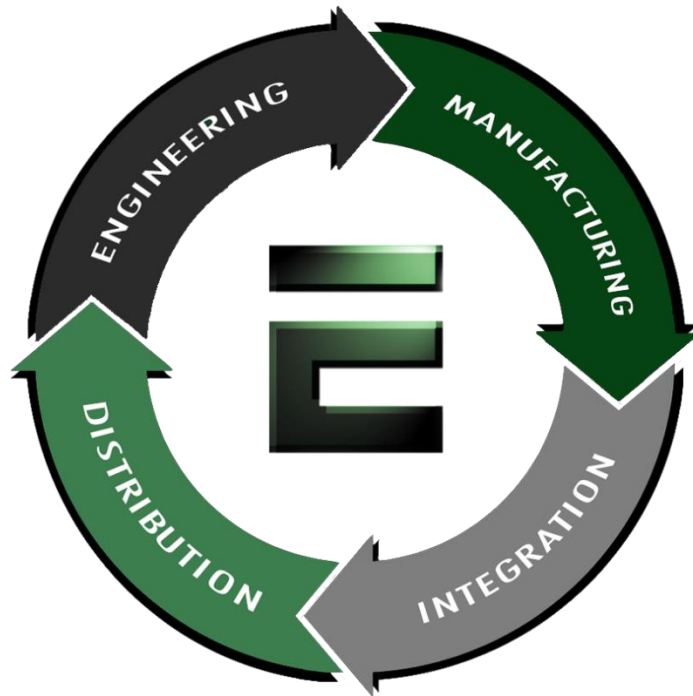


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PFM-541I

AMD® Geode LX800 Processor

AMD LX800 + CS5536

Intel 82551ER/IT for 10/100Mbps

24-bit TFT LCD Panel

4 COM, 4USB

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Packing List

Before you begin installing your card, please make sure that the following materials have been shipped:

- 9681540I02 (for standard)
Cable Kit for PFM-541I
 - 1701160201 VGA cable x 1
 - 1709100201 USB Cable x 1
 - 1700060152 Keyboard & Mouse Cable x 1
 - 1701440350 44-pin IDE Cable x 1
- Quick Installation Guide
- Utility CD (optional)
- PFM-541I

Note:

The PFM-541I WiTAS series include the full cable kit because the special cables can be used in rigid environment.

If any of these items should be missing or damaged, please contact your distributor or sales representative immediately.

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Chapter

1

General Information

1.1 Introduction

We are a leading company in embedded boards manufacturing with a full range of PC/104 CPU Modules, and are proud to launch a brand new PC/104 CPU Module-PFM-541I. Its compact size and rich functionality ensures the most cost effective and compatible module to coincide with your existing system planning devices.

PFM-541I adopts an AMD Geode LX800 processor that is more cost effective compared to other PC/104 CPU modules on the market. Although PFM-541I is a small board, it offers the full functions customers demand. The chipset of PFM-541I deploys AMD LX800 and CS5536 that makes this board achieve high performance. It features two 10/100Base-TX Ethernet ports, four USB 2.0 ports, four serial ports, one parallel port, watchdog timer and includes one PC/104 socket expansion.

1.2 Features

- AMD Geode LX800 Processor
- Onboard DDR SDRAM, Max. 256MB for DDR333
- 18/24-bit TFT LCD panel
- Simultaneous CRT and TFT LCD Display
- 4 USB 2.0 ports, 4 COM ports and 1 Parallel port
- Two 10/100Base-TX Ethernet
- PC/104 Socket Expansion
- +5V/AT only

1.3 Specifications

System

- Processor AMD Geode LX 800 processor
- System Memory Onboard DDR SDRAM,
DDR333 up to 256MB
- Chipset AMD LX 800 + CS5536
- I/O Chipset SMSC SCH3114 (SMSC
3114i-Nu for WiTAS series
products)
- Ethernet Intel 82551ER x 2,
10/100Mbps, 5x2 pin header x 2
(Intel 82551IT for WiTAS series
products)
- BIOS Award 1MB Flash ROM
- Watchdog Timer 255 levels
- RTC CS5536
- Hardware Monitor SMSC SCH3114
- PCI to ISA Solution ITE8888
- Expansion Interface PC/104 socket x 1
- Power Requirement +5V/AT
- Board Size 3.55"(L) x 3.775"(W) (90mm x
96mm)
- Operating Temperature 32°F~ 140°F (0°C ~ 60°C);
-40°F~ 185°F (-40°C ~ 85°C) (for

WiTAS II series products)

Display: Supports CRT/LCD Simultaneous and Dual View Display

- Chipset AMD LX 800 processor integrated graphics
- Resolution Up to 1600 x 1200 for CRT
Up to 1600 x 1200 @ 24-bit for LCD

I/O

- Storage PATA-33 x 1(supports two ATAPI Devices; CompactFlash Type I
- Serial Port Four COM ports: internal pin headers for 4 COM ports, RS-232 x 3 (COM1, COM3, COM4), RS-232/422/485 x 1 (COM2)
- Parallel Port Supports SPP/EPP/ECP mode
- USB Four USB2.0 ports
- PS/2 Port One keyboard and one mouse support

Chapter

2

Quick Installation Guide

Notice:

The Quick Installation Guide is derived from Chapter 2 of user manual. For other chapters and further installation instructions, please refer to the user manual CD-ROM that came with the product.



2.1 Safety Precautions

Warning!

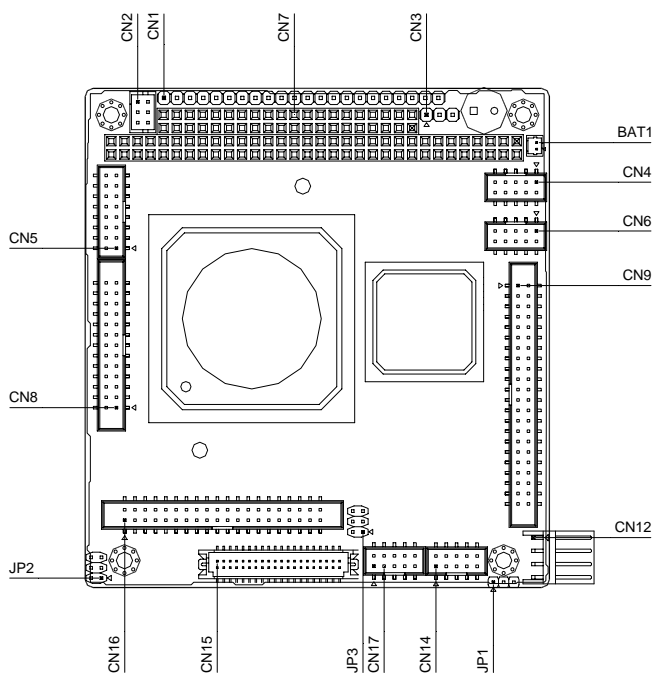
Always completely disconnect the power cord from your board whenever you are working on it. Do not make connections while the power is on, because a sudden rush of power can damage sensitive electronic components.

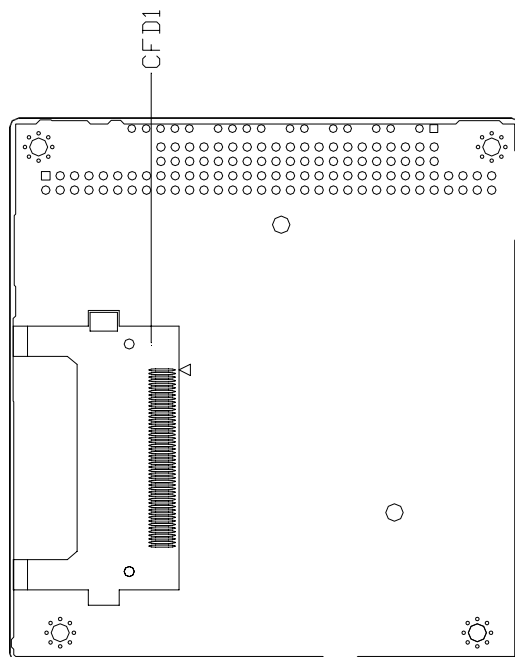
Caution!

Always ground yourself to remove any static charge before touching the board. Modern electronic devices are very sensitive to static electric charges. Use a grounding wrist strap at all times. Place all electronic components on a static-dissipative surface or in a static-shielded bag when they are not in the chassis

2.2 Location of Connectors and Jumpers

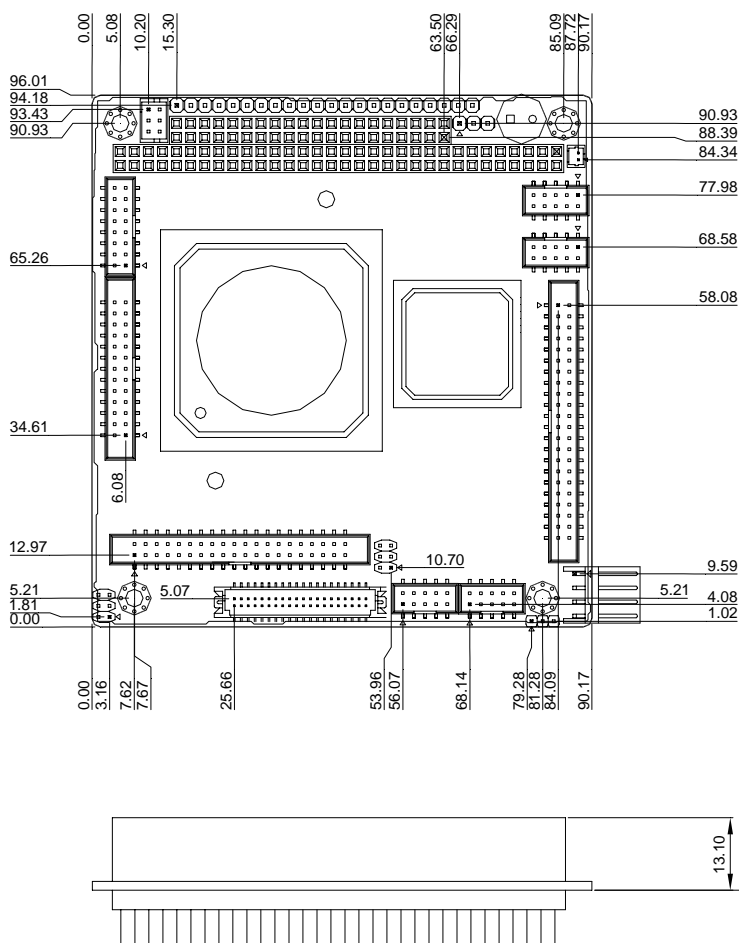
Component Side



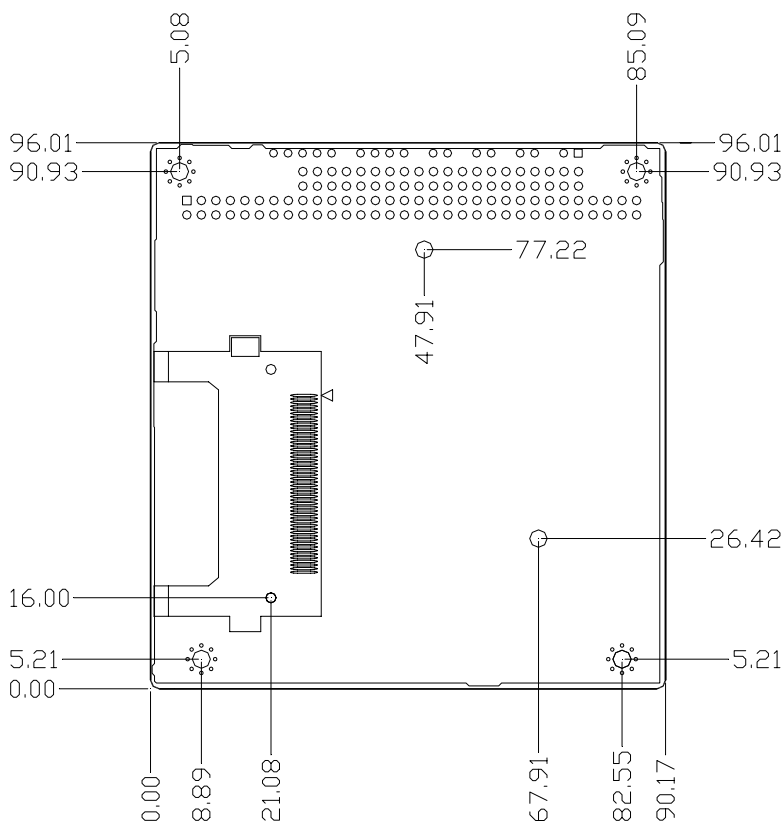
Solder Side

2.3 Mechanical Drawing

Component Side



Solder Side



2.4 List of Jumpers

The board has a number of jumpers that allow you to configure your system to suit your application.

The table below shows the function of each of the board's jumpers:

Jumpers

Label	Function
JP1	CFD Master/Slave Selection
JP2	RS-232 Ring/5V Selection
JP3	LCD Clock Selection and Clear COMS

2.5 List of Connectors

The board has a number of connectors that allow you to configure your system to suit your application. The table below shows the function of each board's connectors:

Note: For further information about mating connectors, please refer to the appendix of manual.

Connectors

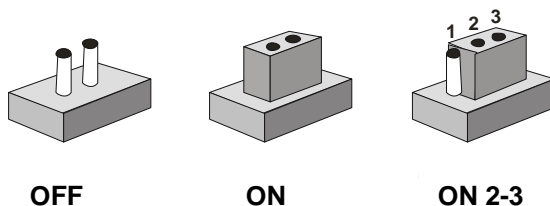
Label	Function
CN1	Front Panel Connector
CN2	PS2 Keyboard/Mouse Connector
CN3	Option Power Connector
CN4	USB Connectors
CN5	VGA Display Connector
CN6	USB Connectors
CN7	PC104 Connector
CN8	LPT Port Connector
CN9	IDE Connector
CN10	N/A
CN11	N/A
CN12	Power Connector
CN13	N/A
CN14	Ethernet Connector
CN15	LCD Connector
CN16	COM1/2/3/4 Connectors

CN17	Ethernet Connector
BAT1	Battery Connector
CFD1	Type1 Compact Flash Slot

2.6 Setting Jumpers

You configure your card to match the needs of your application by setting jumpers. A jumper is the simplest kind of electric switch. It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” a jumper you connect the pins with the clip.

To “open” a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2 and 3. In this case you would connect either pins 1 and 2 or 2 and 3.



A pair of needle-nose pliers may be helpful when working with jumpers. If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any change. Generally, you simply need a standard cable to make most connections.

2.7 CFD Master/Slave Selection (JP1)

JP1	Function
1-2	Slave
2-3	Master (Default)

2.8 RS-232 Ring/5V Selection (JP2)

JP2	Function
COM1	
1-3	+5V
3-5	Ring (Default)
COM2	
2-4	+5V
4-6	Ring (Default)

2.9 LCD Clock Selection and Clear CMOS (JP3)

JP3	Function
LCD Clock	
1-3	Normal (Default)
3-5	Reverse
CMOS	
2-4	Protected (Default)
4-6	Clear

2.10 Front Panel Connector (CN1)

Pin	Signal
1	Power LED(+)
2	Power LED(-)
3	N.C
4	Speaker(+)
5	Speaker(-)
6	N.C
7	IDE LED(+)
8	IDE LED(-)
9	N.C
10	Reset(+)
11	Reset(-)
12	N.C
13	Lan1 Speed LED(+)
14	Lan1 Speed LED(-)
15	Lan1 Active LED(+)
16	Lan1 Active LED(-)
17	N.C
18	N.C
19	Lan2 Speed LED(+)
20	Lan2 Speed LED(-)
21	Lan2 Active LED(+)

22	Lan2 Active LED(-)
----	--------------------

2.11 PS2 Keyboard/Mouse Connector (CN2)

Pin	Signal	Pin	Signal
1	KB_DATA	2	KB_CLK
3	GND	4	+5V
5	MS_DATA	6	MS_CLK

2.12 Optional Power Connector (CN3)

Pin	Signal
1	-12V
2	-5V
3	GND

2.13 USB (3-4) Connector (CN4)

Pin	Signal	Pin	Signal
1	+5V	2	GND
3	USBD3-	4	GND
5	USBD3+	6	USBD4+
7	GND	8	USBD4-
9	GND	10	+5V

2.14 VGA Display Connector (CN5)

Pin	Signal	Pin	Signal
1	RED	2	+5V
3	GREEN	4	GND
5	BLUE	6	N.C
7	N.C	8	DDCDAT
9	GND	10	HSYNC
11	GND	12	VSYN
13	GND	14	DDCCLK
15	GND	16	GND

2.15 USB (1-2) Connector (CN6)

Pin	Signal	Pin	Signal
1	+5V	2	GND
3	USBD1-	4	GND
5	USBD1+	6	USBD2+
7	GND	8	USBD2-
9	GND	10	+5V

2.16 PC/104 Connector (CN7)

As standard.

Note: PFM-541I does not support ISA card with DMA or Master mode in PC/104 interface.

2.17 LPT Port Connector (CN8)

Pin	Signal	Pin	Signal
1	#STROBE	2	#AFD
3	DATA0	4	#ERROR
5	DATA1	6	#INIT
7	DATA2	8	#SLIN
9	DATA3	10	GND
11	DATA4	12	GND
13	DATA5	14	GND
15	DATA6	16	GND
17	DATA7	18	GND
19	#ACK	20	GND
21	BUSY	22	GND
23	PE	24	GND
25	SELECT	26	N.C

2.18 IDE Connector (CN9)

Pin	Signal	Pin	Signal
1	#RESET	2	GND
3	DATA7	4	DATA8
5	DATA6	6	DATA9
7	DATA5	8	DATA10
9	DATA4	10	DATA11
11	DATA3	12	DATA12

13	DATA2	14	DATA13
15	DATA1	16	DATA14
17	DATA0	18	DATA15
19	GND	20	N.C
21	REQ	22	GND
23	#IOW	24	GND
25	#IOR	26	GND
27	IOREADY	28	GND
29	#DACK	30	GND
31	IRQ	32	N.C
33	ADDR1	34	DETECT
35	ADDR0	36	ADDR2
37	#CS0	38	#CS1
39	HDDLED	40	GND
41	+5V	42	+5V
43	GND	44	N.C

Note: The IDE interface on PFM-541I only supports two IDE devices. If you use a CF card, there is only one IDE device usable.

2.19 Power Connector (CN12)

Pin	Signal
1	+5V
2	GND
3	GND

4	+12V
---	------

2.20 Ethernet Connector (CN14)

Pin	Signal	Pin	Signal
1	RX-	2	RX+
3	N.C	4	N.C
5	CHASSIS_GND	6	CHASSIS_GND
7	N.C	8	N.C
9	TX+	10	TX-

2.21 LCD Connector (CN15)

24-bit TTL

Pin	Signal	Pin	Signal
1	+5V	2	+5V
3	GND	4	GND
5	+3.3V	6	+3.3V
7	ENBKL	8	GND
9	BLUE0	10	BLUE1
11	BLUE2	12	BLUE3
13	BLUE4	14	BLUE5
15	BLUE6	16	BLUE7
17	GREEN0	18	GREEN1
19	GREEN2	20	GREEN3
21	GREEN4	22	GREEN5

23	GREEN6	24	GREEN7
25	RED0	26	RED1
27	RED2	28	RED3
29	RED4	30	RED5
31	RED6	32	RED7
33	GND	34	GND
35	CLOCK	36	VSYN
37	DE	38	HSYN
39	N.C	40	ENAVEE

18-bit TTL

Pin	Signal	Pin	Signal
1	+5V	2	+5V
3	GND	4	GND
5	+3.3V	6	+3.3V
7	ENBKL	8	ENBKL
9	N.C	10	N.C
11	BLUE0	12	BLUE1
13	BLUE2	14	BLUE3
15	BLUE4	16	BLUE5
17	N.C	18	N.C
19	GREEN0	20	GREEN1
21	GREEN2	22	GREEN3
23	GREEN4	24	GREEN5

25	N.C	26	N.C
27	RED0	28	RED1
29	RED2	30	RED3
31	RED4	32	RED5
33	GND	34	GND
35	CLOCK	36	CLOCK
37	DE	38	DE
39	N.C	40	N.C

2.22 COM1/2/3/4 Connectors (CN16)

Pin	Signal	Pin	Signal
1	DCD	2	DSR
3	RX	4	RTS
5	TX	6	CTS
7	DTR	8	RI/5V
9	GND	10	N.C
11	DCD (485/422_TX-)	12	DSR
13	RX (422_RX+)	14	RTS
15	TX (485/422_TX+)	16	CTS
17	DTR (422_RX-)	18	RI/5V
19	GND	20	N.C
21	DCD	22	DSR
23	RX	24	RTS
25	TX	26	CTS

PC/104 CPU Module		P F M - 5 4 1 I	
-------------------	--	-----------------	--

27	DTR	28	RI
29	GND	30	N.C
31	DCD	32	DSR
33	RX	34	RTS
35	TX	36	CTS
37	DTR	38	RI
39	GND	40	N.C

2.23 Ethernet Connector (CN17)

Pin	Signal	Pin	Signal
1	RX-	2	RX+
3	N.C	4	N.C
5	CHASSIS_GND	6	CHASSIS_GND
7	N.C	8	N.C
9	TX+	10	TX-

2.24 Battery Connector (BAT1)

Pin	Signal	Pin	Signal
1	+3V	2	GND

Chapter

3

Award BIOS Setup

3.1 System Test and Initialization

These routines test and initialize board hardware. If the routines encounter an error during the tests, you will either hear a few short beeps or see an error message on the screen. There are two kinds of errors: fatal and non-fatal. The system can usually continue the boot up sequence with non-fatal errors. Non-fatal error messages usually appear on the screen along with the following instructions:

Press <F1> to RESUME

Write down the message and press the F1 key to continue the boot up sequence.

System configuration verification

These routines check the current system configuration against the values stored in the CMOS memory. If they do not match, the program outputs an error message. You will then need to run the BIOS setup program to set the configuration information in memory.

There are three situations in which you will need to change the CMOS settings:

1. You are starting your system for the first time
2. You have changed the hardware attached to your system
3. The CMOS memory has lost power and the configuration information has been erased.

The PFM-541I CMOS memory has an integral lithium battery backup for data retention. However, you will need to replace the complete unit when it finally runs down.

3.2 Award BIOS Setup

Awards BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backed CMOS RAM so that it retains the Setup information when the power is turned off.

Entering Setup

Power on the computer and press immediately. This will allow you to enter Setup.

Standard CMOS Features

Use this menu for basic system configuration. (Date, time, IDE, etc.)

Advanced BIOS Features

Use this menu to set the advanced features available on your system.

Advanced Chipset Features

Use this menu to change the values in the chipset registers and optimize your system performance.

Integrated Peripherals

Use this menu to specify your settings for integrated peripherals. (Primary slave, secondary slave, keyboard, mouse etc.)

Power Management Setup

Use this menu to specify your settings for power management. (HDD power down, power on by ring, KB wake up, etc.)

PnP/PCI Configurations

This entry appears if your system supports PnP/PCI.

PC Health Status

This menu allows you to set the shutdown temperature for your

system.

Frequency/Voltage Control

Use this menu to specify your settings for auto detect DIMM/PCI clock and spread spectrum.

Load Fail-Safe Defaults

Use this menu to load the BIOS default values for the minimal/stable performance for your system to operate.

Load Optimized Defaults

Use this menu to load the BIOS default values that are factory settings for optimal performance system operations. While AWARD has designated the custom BIOS to maximize performance, the factory has the right to change these defaults to meet their needs.

Set Supervisor/User Password

Use this menu to set Supervisor/User Passwords.

Save and Exit Setup

Save CMOS value changes to CMOS and exit setup.

Exit Without Saving

Abandon all CMOS value changes and exit setup.

Chapter

4

Driver Installation

4.1 Software Drivers

This chapter describes the operation and installation of the display drivers supplied on the Supporting CD-ROM that are shipped with your product. The onboard VGA adapter is based on the AMD LX VGA Flat Panel/CRT controller. This controller offers a large set of extended functions and higher resolutions. The purpose of the enclosed software drivers is to take advantage of the extended features of the AMD LX VGA Flat Panel/CRT controller.

Hardware Configuration

Some of the high-resolution drivers provided in this package will work only in certain system configurations. If a driver does not display correctly, try the following:

1. Change the display controller to CRT-only mode, rather than flat panel or simultaneous display mode. Some high-resolution drivers will display correctly only in CRT mode.
2. If a high-resolution mode does not support your system, try to use a lower-resolution mode. For example, 1024 x 768 mode will not work on some systems, but 800 x 600 mode supports the most.

4.2 Necessary to Know

The instructions in this manual assume that you understand elementary concepts of MS-DOS and the IBM Personal Computer. Before you attempt to install any driver from the *Supporting CD-ROM*, you should:

- Know how to copy files from a CD-ROM to a directory on the hard disk
- Understand the MS-DOS directory structure

If you are uncertain about any of these concepts, please refer to the DOS or OS/2 user reference guides for more information before you proceed with the installation.

Before you begin

The Supporting CD-ROM contains different drivers for corresponding Windows OS, please choose the specific driver for your Windows OS.

4.3 Installing VGA Driver

Win XP / Win XPe VGA

Place the Driver CD-ROM into your CD-ROM drive and follow the steps below to install.

1. Click on **Start** button.
2. Click on **Settings** button.
3. Click on **Control Panel** button.
4. Click on **System** button.
5. Select **Hardware** and click on **Device Manager....**
6. Double click on **Video Controller (VGA Compatible).**
7. Click on **Update Driver....**
8. Click on **Next.**
9. Select **Search for a suitable driver...**, then click on **Next.**
10. Select **Specify a location**, then click on **Next.**
11. Click on **Browse.**
12. Select "lx_win" file from CD-ROM (**Driver/Step 1 – LX_Graphics**) then click on **Open.**
13. Click on **OK.**
14. Click on **Next.**
15. Click on **Yes.**
16. Click on **Finish.**

Note: The user must install this system driver before install other device drivers.

4.4 Installing AES Driver

Win XP / Win XPe Geode LX AES Crypto

Place the Driver CD-ROM into your CD-ROM drive and follow the steps below to install.

1. Click on **Start** button.
2. Click on **Settings** button.
3. Click on **Control Panel** button.
4. Click on **System** button.
5. Select **Hardware** and click on **Device Manager....**
6. Double click on **Entertainment Encryption/Decryption Controller**.
7. Click on **Update Driver....**
8. Click on **Next**.
9. Select **Search for a suitable driver...**, then click on **Next**.
10. Select **Specify a location**, then click on **Next**.
11. Click on **Browse**.
12. Select **“GeodeLX_XP_WDM_AES_v2.01.00”** file from CD-ROM (**Driver/Step 2 – AES**) then click on **Open**.
13. Click on **OK**.
14. Click on **Next**.
15. Click on **Yes**.
16. Click on **Finish**.

4.5 Installing PCI to ISA Bridge Driver

Win XP / Win XPe System

Place the Driver CD-ROM into your CD-ROM drive and follow the steps below to install.

1. Click on **Start** button.
2. Click on **Settings** button.
3. Click on **Control Panel** button.
4. Click on **System** button.
5. Select **Hardware** and click on **Device Manager....**
6. Double click on **Other PCI Bridge Device**
7. Click on **Update Driver....**
8. Click on **Next**.
9. Select **Search for a suitable driver...**, then click on **Next**.
10. Select **Specify a location**, then click on **Next**.
11. Click on **Browse**.
12. Select "**lte**" file from CD-ROM (**Driver/Step 3 – PCI to ISA Bridge**) then click on **open**.
13. Click on **OK**.
14. Click on **Next**.
15. Click on **Finish**.

4.6 Installing Ethernet Driver

Place the Driver CD-ROM into your CD-ROM drive and follow the steps below to install.

1. Click on **Start** button.
2. Click on **Settings** button.
3. Click on **Control Panel** button.
4. Click on **System** button.
5. Select **Hardware** and click on **Device Manager....**
6. Double click on **Ethernet Controller**.
7. Click on **Update Driver....**
8. Click on **Next**.
9. Select **Search for a suitable driver....**, then click on **Next**.
10. Select **Specify a location**, then click on **Next**.
11. Click on **Browse**.
12. Select "Select **Intel 82551ER Driver**" folder from CD-ROM
(**Driver/Step 4 - Intel LAN driver**) then click on **Open**.
13. Click on **OK**.
14. Click on **Next**.
15. Click on **Yes**.
16. Click on **Finish**

Appendix

A

Programming the Watchdog Timer

A.1 Programming

PFM-541I utilizes SCH3114-NU chipset as its watchdog timer controller.

The SCH311X WDT (Watch Dog Timer) has a programmable time-out ranging from 1 to 255 minutes with one minute resolution, or 1 to 255 second resolution. The unit of the WDT timeout value are selected via bit[7] of the WDT_TIMEOUT register. The WDT time-out value is set through the WDT_VAL Runtime register. Setting The WDT_VAL register to 0x00 disables the WDT function (this is its power on default).

Setting the WDT_VAL to any other non-zero value will cause the WDT to reload and begin counting down from the value loaded. When the WDT count value reaches zero the counter stops and sets the Watchdog time-out status bit in the WDT_CTRL Runtime register. Note: Regardless of the current state of the WDT, the WDT time-out status bit can be directly set or cleared by the Host CPU.

The related register for configuring WDT is list as follows:

NAME	REG OFFSET (HEX)	DESCRIPTION
GP60 Default = 0x01 on VTR POR	47 (R/W)	General Purpose I/O bit 6.0 Bit[0] In/Out : =1 Input, =0 Output Bit[1] Polarity : =1 Invert, =0 No Invert Bit[3:2] Alternate Function Select 11=WDT 10=Either Edge Triggered Interrupt Input 4 (Note 26.20) 01=LED1 00=GPIO Bits[6:4] Reserved Bit[7] Output Type Select 1=Open Drain 0=Push Pull

WDT_TIME_OUT Default = 0x00 on VCC POR, VTR POR, and PCI Reset	65 (R/W)	Watch-dog Timeout Bit[0] Reserved Bit[1] Reserved Bits[6:2] Reserved, = 00000 Bit[7] WDT Time-out Value Units Select = 0 Minutes (default) = 1 Seconds
WDT_VAL Default = 0x00 on VCC POR, VTR POR, and PCI Reset	66 (R/W)	Watch-dog Timer Time-out Value Binary coded, units = minutes (default) or seconds, selectable via Bit[7] of WDT_TIME_OUT register (0x52). 0x00 Time-out disabled 0x01 Time-out = 1 minute (second) 0xFF Time-out = 255 minutes (seconds)

NAME	REG OFFSET (HEX)	DESCRIPTION
WDT_CFG Default = 0x00 on VCC POR, VTR POR, and PCI Reset	67 (R/W)	Watch-dog timer Configuration Bit[0] Reserved Bit[1] Keyboard Enable =1 WDT is reset upon a Keyboard interrupt. =0 WDT is not affected by Keyboard interrupts. Bit[2] Mouse Enable =1 WDT is reset upon a Mouse interrupt. =0 WDT is not affected by Mouse interrupts. Bit[3] Reserved Bits[7:4] WDT Interrupt Mapping 1111 = IRQ15 0011 = IRQ3 0010 = IRQ2 (Note) 0001 = IRQ1 0000 = Disable Note: IRQ2 is used for generating SMI events via the serial IRQ's stream. The WDT should not be configured for IRQ2 if the IRQ2 slot is enabled for generating an SMI event.
WDT_CTRL Default = 0x00 on VCC POR and VTR POR Default = 0000000xb on PCI Reset Note: Bit[0] is not cleared by PCI Reset	68 (R/W) Bit[2] is Write-Only	Watch-dog timer Control Bit[0] Watch-dog Status Bit, R/W =1 WD timeout occurred =0 WD timer counting Bit[1] Reserved Bit[2] Force Timeout, W =1 Forces WD timeout event; this bit is self-clearing Bit[3] P20 Force Timeout Enable, R/W = 1 Allows rising edge of P20, from the Keyboard Controller, to force the WD timeout event. A WD timeout event may still be forced by setting the Force Timeout Bit, bit 2. Note: If the P20 signal is high when the enable bit is set a WD timeout event will be generated. = 0 P20 activity does not generate the WD timeout event. Note: The P20 signal will remain high for a minimum of 1us and can remain high indefinitely. Therefore, when P20 forced timeouts are enabled, a self- clearing edge-detect circuit is used to generate a signal which is OR'ed with the signal generated by the Force Timeout Bit. Bit[7:4] Reserved. Set to 0

The following is a sample code to set WDT for 3 seconds.

;Runtime register I/O base address

SUPERIO_GPIO_PORT EQU 800h

.MODEL SMALL

.CODE

begin:

;enable WDT

mov dx, SUPERIO_GPIO_PORT + 47h

mov al, 0Ch

out dx, al

;WDT_TIME_OUT register

mov dx, SUPERIO_GPIO_PORT + 65h

mov al, 80h ;unit is second

out dx, al

;WDT_VAL register

mov dx, SUPERIO_GPIO_PORT + 66h

mov al, 03h ;3 seconds

out dx, al

;exit

mov ah, 4ch

int 21h

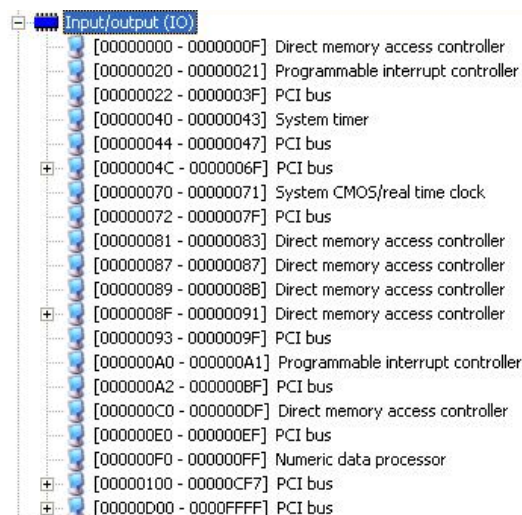
END begin

Appendix

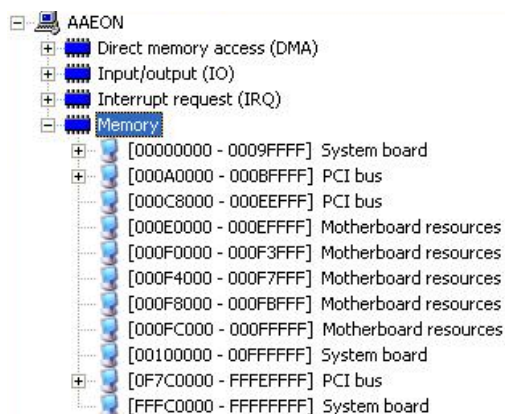
B

I/O Information





















B.1 I/O Address Map



B.2 1st MB Memory Address Map



B.3 IRQ Mapping Chart

+		Direct memory access (DMA)
+		Input/output (IO)
-		Interrupt request (IRQ)
		(ISA) 0 System timer
		(ISA) 1 PC/AT Enhanced PS/2 Keyboard (101/102-Key)
		(ISA) 3 Communications Port (COM2)
		(ISA) 4 Communications Port (COM1)
		(ISA) 6 Standard floppy disk controller
		(ISA) 8 System CMOS/real time clock
		(ISA) 10 Communications Port (COM3)
		(ISA) 11 Communications Port (COM4)
		(ISA) 12 Microsoft PS/2 Mouse
		(ISA) 13 Numeric data processor
		(ISA) 14 Primary IDE Channel
		(PCI) 5 Intel(R) 8255xER PCI Adapter
		(PCI) 5 Standard Enhanced PCI to USB Host Controller
		(PCI) 5 Standard OpenHCD USB Host Controller
		(PCI) 9 Advanced Micro Devices Win XP Graphics Driver
		(PCI) 9 Geode LX AES Crypto Driver
		(PCI) 9 Intel(R) 8255xER PCI Adapter #2

B.4 DMA Channel Assignments

-		AAEON
-		Direct memory access (DMA)
		2 Standard floppy disk controller
		4 Direct memory access controller
+		Input/output (IO)
+		Interrupt request (IRQ)
+		Memory

Appendix

C

Mating Connecotor

C.1 List of Mating Connectors and Cables

The table notes mating connectors and available cables.

Connector Label	Function	Mating Connector		Available Cable	MFG Cable P/N
		Vendor	Model no		
CN1	Front Panel Connector	Neltron	2226A-XX		N/A
CN2	PS2 Keyboard/Mouse Connector	Neltron	2026B--XX	Keyboard & Mouse Cable	1700060152 1700060154*
CN3	Option Power Connector	Dupont	2541-1H		N/A
CN4	USB Connector	Neltron	2026B--XX	USB Cable	1709100201 170910020D *
CN5	VGA Display Connector	Neltron	2026B--XX	CRT Cable	1701160201
CN6	USB Connector	Neltron	2026B--XX	USB Cable	1709100201 170910020D *
CN8	LPT Port Connector	Keentop	1014 Series	Parallel Port Cable	1701260201
CN9	IDE Connector	Keentop	1014 Series	IDE Cable	1701440350
CN12	Power Connector	Ever	2542H-04		N/A
CN14	Ethernet Connector	Keentop	1014 Series	Ethernet Cable	1700100200 1700100209 *
CN15	LCD Connector	HO TIEN	1255H		N/A
CN16	COM1/2/3/4 Connector	Keentop	1014 Series	Serial Port Cable	1701400250
CN17	Ethernet Connector	Keentop	1014 Series	Ethernet Cable	1700100200
BAT1	Battery Connector	HRS	DF14-MS-1.25C		N/A

Note:

The Cable P/N with “*” sign is for WiTAS series products.