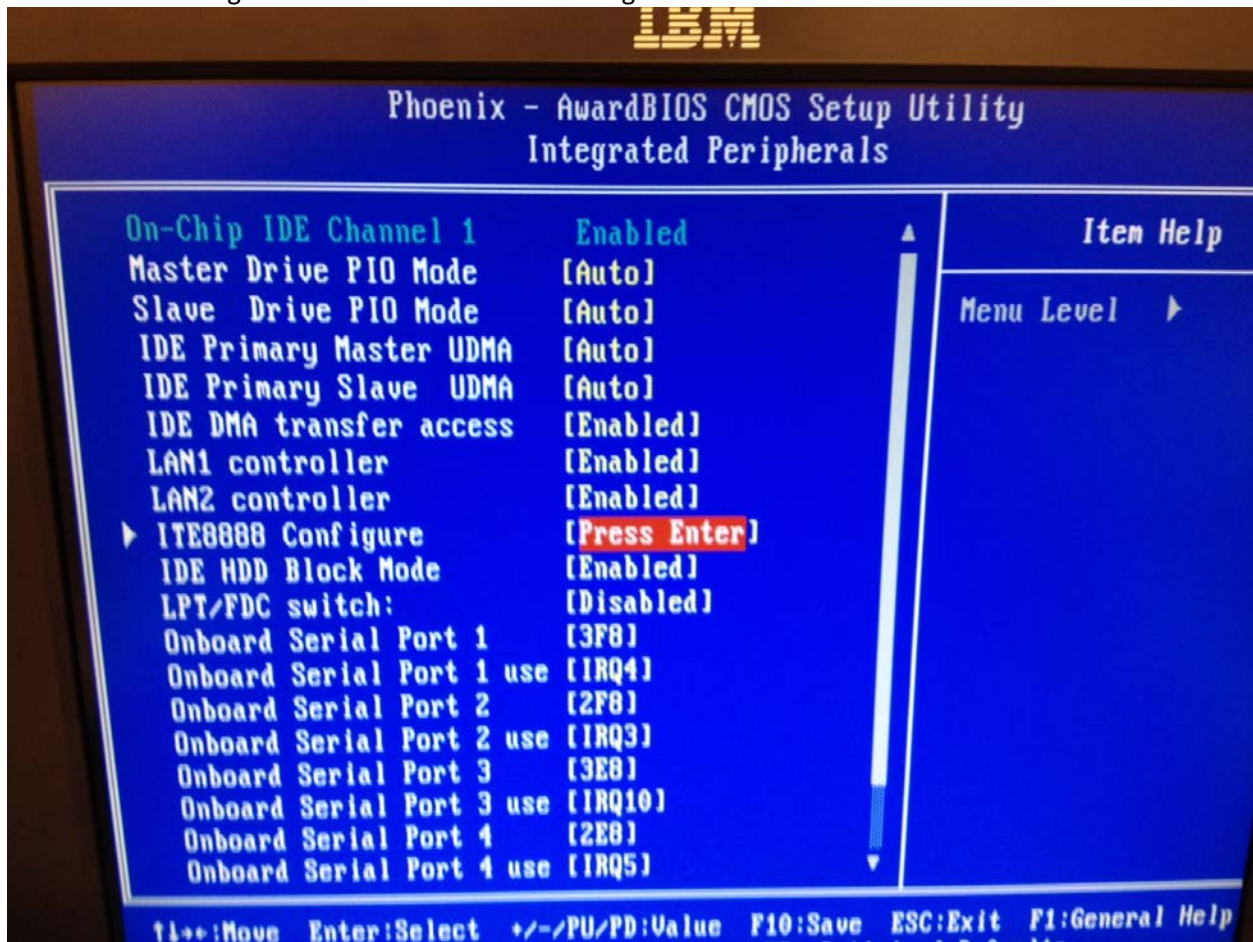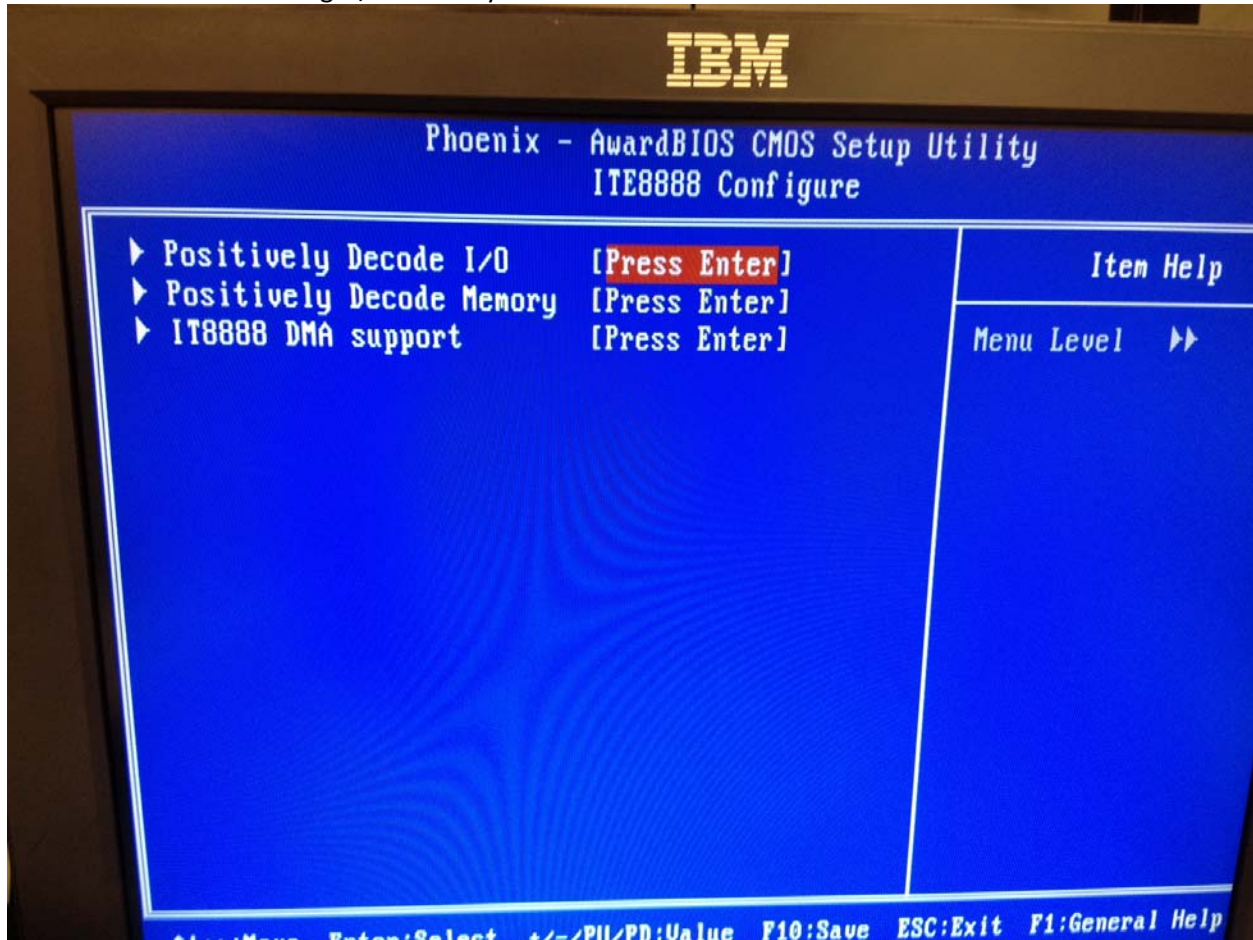On the PCM-9375 there is an PCI to ISA bridge chip to allow ISA functions on the LX800. To open ISA IO addresses , memory ranges and DMA for ISA expansion cards "PC/104 boards" to function it is required to adjust the ITE8888 settings in the CMOS.
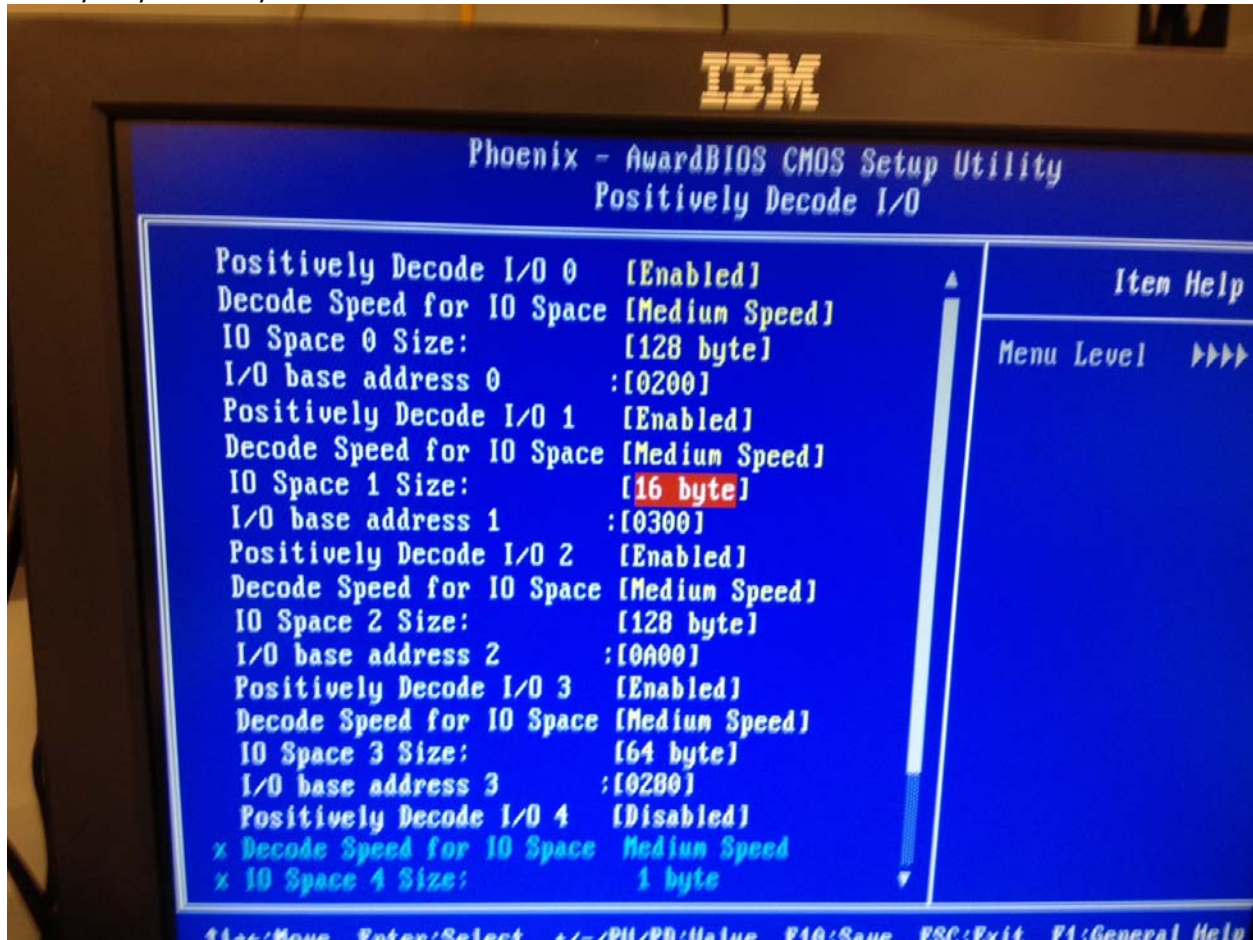The ITE8888 settings are found in the ITE8888 Configure section of the CMOS:

The menu has three settings I/O Memory and DMA:

Phoenix - AwardBIOS CMOS Setup Utility
ITE8888 Configure

▶ Positively Decode I/O   [Press Enter]   Item Help
▶ Positively Decode Memory [Press Enter]
▶ IT8888 DMA support   [Press Enter]   Menu Level ▶▶

↑↓→←:Move   Enter:Select   +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help

In the "Positively decode I/O" section settings can be adjusted to open ISA IO addresses, if for example you need a space opened at 0x320H for 16 bytes you would select
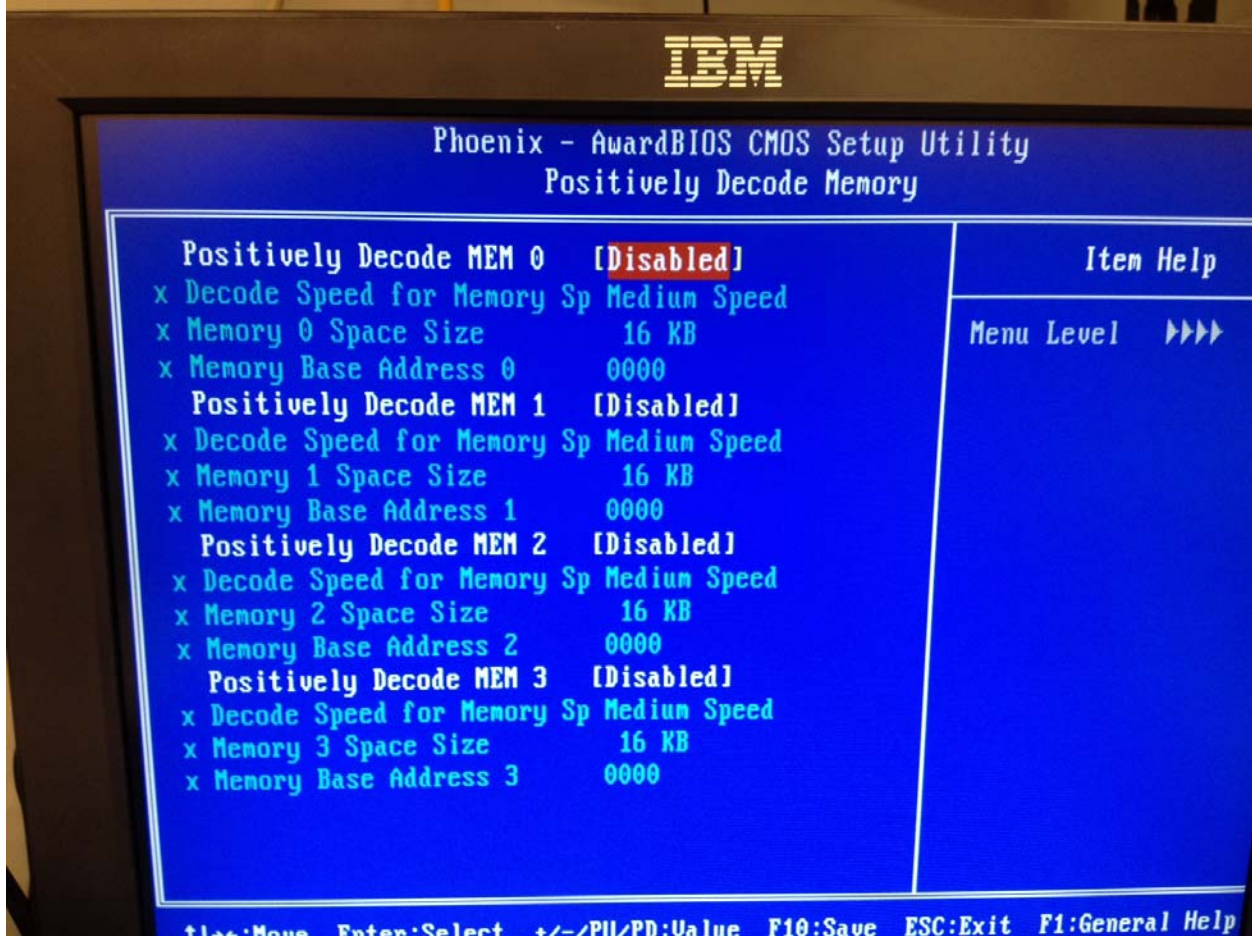
- "Positively Decode I/O 4" and set it to enable
- Set the decode speed, (typically medium speed is adequate for ISA)
- Set in the IO space 4 size (this example is 16 bytes)
- Enter the I/O base address (this example is 320)

Here is a picture of the standard settings, unless expanding the IO space, do not change these settings, as they may adversely affect the function of the board.

The Decode memory is similar to the IO, but it opens holes in memory for cards to transfer data (similar to the 15MB memory hole)

DMA settings are Just the DMA channels to enable or disable for DMA channel transfers.