PCM-5824

NS Geode Single Board
Computer with CPU SVGA/DSTN
LCD, Ethernet and Audio Interface

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This manual is for the PCM-5824 Rev. A102 or higher

Packing list

Before you begin installing your card, please make sure that the following materials have been shipped:

- 1 PCM-5824 Series all-in-one single board computer
- 1 startup manual
- 1 utility disk/CD, driver, and manual (in PDF format)
- 1 2.5" IDE flat cable, 44-pin to 44-pin
- 1 keyboard / PS2 mouse cable
- 1 secondary serial port cable
- 1 parallel cable
- 1 floppy cable, for 3.5" FDD only
- 1 audio cable
- 1 USB cable

If any of these items are missing or damaged, contact your distributor or sales representative immediately.

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General Information

This chapter gives background information on the PCM-5824.

Sections include:

• Board specifications

• Board layout and dimensions

1.1 Introduction

The PCM-5824 is the ultimate cost-effective solution for limited space applications. It is a new NS Geode 3.5" Biscuit PC with enhanced graphics function. It uses an Asiliant (C&T) 69000 chip which supports TFT, STN and Mono LCDs. It offers all the functions of an AT-compatible industrial computer on a single board and only occupies the space of a 3½" hard drive. The PCM-5824 comes with an embedded high-performance GXLV-200 or GX1-300 processor on-board. For maximum performance, the PCM-5824 Series also support an SDRAM SODIMM socket that can accept up to 128 MB memory.

On-board features include an Ethernet interface, audio interface, socket for Compact Flash Card, Enhanced IDE interface with up to Ultra DMA transfer protocol, one parallel port, two serial ports (RS-232 and RS-232/422/485) with DB-9 connector as COM1, and a mini-DIN PS/2 keyboard/mouse interface. A SVGA/LCD display controller (LCD, and CRT displays) allows LCD screen resolutions up to 1024 x 768 and CRT resolutions up to 1280 x 1024 @ 16 colors.

The PCM-5824 complies with the "Green Function" standard and supports three types of power saving features: Normal, Doze, and Sleep modes.

The display type configuration is done through software. A single Flash chip holds the system BIOS and the VGA BIOS. This minimizes the number of chips and eases configuration. You can change the display BIOS simply by programming the Flash chip.

If you need any additional functions, the PCM-5824 has a PC/104 connector for future upgrades.

1.2 Features

- Ultra-compact size single board computer as small as a 3 1/2" hard disk drive (145 mm x 102 mm)

- On-board NS GX1-300 or GXLV-200 CPU

- Up to 128 MB system memory by SODIMM (SDRAM)

- On-board VGA/LCD controller

- On-board 10/100 Mbps Ethernet interface

- Supports CompactFlash card

- Built-in Enhanced IDE (AT bus) hard disk drive interface

- On-board mini-DIN PS/2 keyboard/mouse connector

- Two serial ports: one RS-232, one RS-232/422/485 or infared selectable (uses 16C550 UARTs with 16 byte FIFO)

- Upgradeable through PC/104 module

- Green engine with sleep mode and low power consumption

- Single +5 V power supply

1.3 Specifications

1.3.1 Standard SBC functions

- **CPU**:
  - Embedded NS GXLV-200 PCM-5824-D0A1
  - Embedded NS GX1-300 PCM-5824-G0A1

- **BIOS**: AWARD 256 KB Flash memory

- **Chipset**: NS CX5530

- **System memory**: One 144-pin SODIMM socket accepts up to 128 MB SDRAM

- **Enhanced IDE interface**: Supports up to two EIDE devices. BIOS auto-detect, PIO Mode 3 or Mode 4 transfer, Ultra DMA33 mode (ATA-4) up to 33 MB/sec.

- **FDD interface**: Supports up to two FDDs

- **Serial ports**: One serial RS-232 port, one serial RS-232/422/485 port

- **Parallel port**: One parallel port, supports SPP/EPP/ECP mode

- **Infrared port**: Shared with COM2. Transfer rate up to 115 kbps.

- **Keyboard/mouse connector**: Mini-DIN connector supports standard PC/AT keyboard and a PS/2 mouse

- **USB interface**: two USB ports, USB 1.0 compliant

- **Power management**: Supports power saving modes including Normal/Doze/Sleep modes. APM 1.1 compliant

- **Watchdog timer**: 1.6 sec. intervals

1.3.2 Local-bus flat panel/VGA interface

- **Chipset**: Asiliant 69000 2 MB SDRAM on chip.

  Optional Asiliant 69030 with 4 MB memory on chip.

- **Display memory**: 1 ~ 4 MB share memory, set in BIOS

- **Display type**: Supports CRT and TFT LCD displays. Can display CRT and flat panel simultaneously

- **Flat panel display mode:** Panel resolution supports up to 1024 x 768 @ 18 bpp. Supports 36-bit TFT LCD panel

- **CRT display mode**: Non-interlaced CRT monitors resolutions up to 1280 x 1024 @ 256 colors or 1024 x 768 @ 16 bpp

- **Dual display**: Supports CRT and LCD displays under Windows® 98/Windows® 2000 (69030 only)

1.3.3 Audio function

- **Chipset**: NS CX5530

- **Audio controller**: AC97 version 2.0 compliant interface

- **Audio interface**: Microphone in, Line in, CD audio in, Line out, Speaker L, Speaker R.

- **Power**: Accepts +12 V source for improved audio quality

1.3.4 Ethernet interface

- **Chipset**: RTL 8139

- **Ethernet interface**: PCI 10/100 Mbps Ethernet. IEEE 802.3 u protocol compatible

- **Connection**: On-board RJ-45 connector

- I/O address switchless setting

- Built-in boot ROM

1.3.5 Mechanical and environmental

- **Dimensions (L x W)**: 145 mm x 102 mm (5.9" x 4.2")

- **Power supply voltage**: +5 V (4.75 ~ 5.25 V)

- **Power consumption** (typical) :
  +5 V @ 1.5 A (typical) (for PCM-5824 w/ GXLV-200)

  +5 V @ 4 A (Max)

- **Operating temperature**: 0 ~ 60° C (32 ~ 140° F)

- **Weight**: 0.77 kg (weight of total package)

1.3.6 Solid state disk

- Supports one 50-pin socket for CompactFlash™ card

1.4 Board layout and dimensions
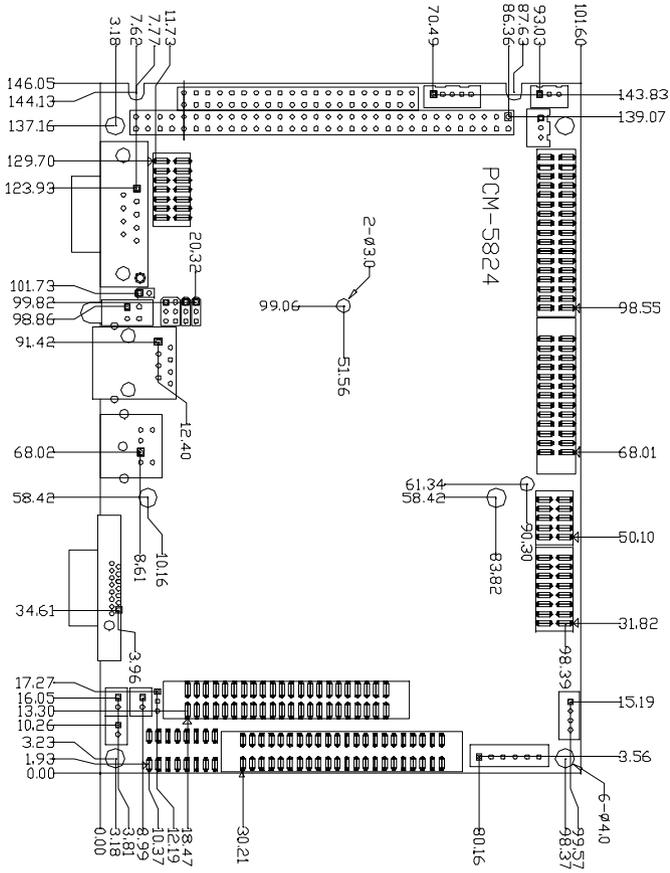
Figure 1-1: PCM-5824 dimensions

Installation

This chapter tells how to set up the PCM-5824 hardware, including instructions on setting jumpers and connecting peripherals, switches and indicators. Be sure to read all the safety precautions before you begin the installation procedure.

2.1 Jumpers and connectors

Connectors on the board link it to external devices such as hard disk drives, a keyboard or expansion bus connectors. In addition, the board has a number of jumpers that allow you to configure your system to suit your application.

The table below lists the function of each of the board jumpers and connectors:

Table 2-1: Jumpers

Label	Function
J2	Clear CMOS
J3	Watchdog timer action
J4	COM2 setting
J5	Buzzer setting
J6	LCD power selector
J7	External HDD LED (pin header)
J8	External power LED (pin header)
J9	External reset pin (pin header)
SW1	LCD Panel type select

Table 2-2: Connectors

Label	Function
CN1	CD audio input connector
CN2	Floppy disk connector
CN3	Parallel port connector
CN4	USB connector
CN5	Audio connector
CN6	CPU fan power connector (5820/L)
CN7	IR connector (infrared)
CN8	PC/104 connector
CN9	Main power connector (+5 V, +12 V)
CN10	Auxilary (peripheral) power connector (-5 V, -12 V)
CN11	IDE hard disk connector
CN13	Ethernet connector
CN14	COM2 connector
CN16	COM1 connector
CN17	PS/2 keyboard + PS/2 mouse
CN18	CRT display connector
CN19	CompactFlash socket
CN20	SODIMM socket
CN21	24 bit LCD connector
CN22	External LCD connector
LED1	Ethernet LED
SW1	LCD type select

2.2 Locating jumpers
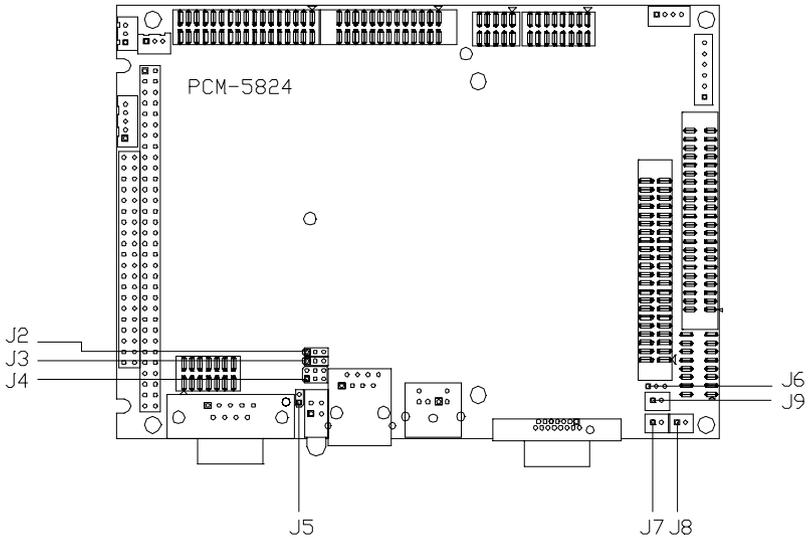
Figure 2-1: Jumpers PCM-5824

2.3 Locating connectors

2.3.1 Component side
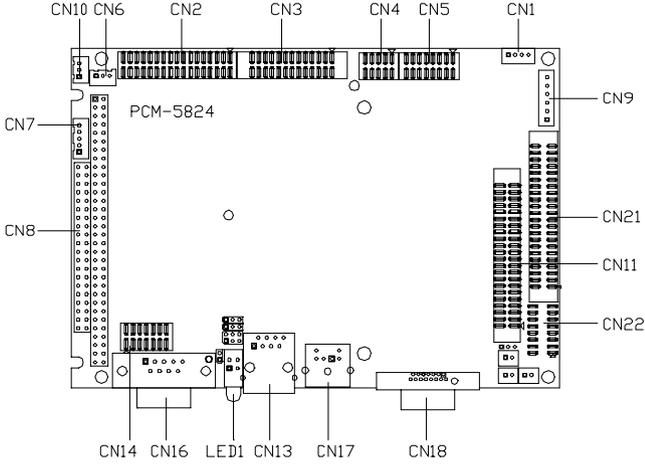
Figure 2-2: Connectors - component side
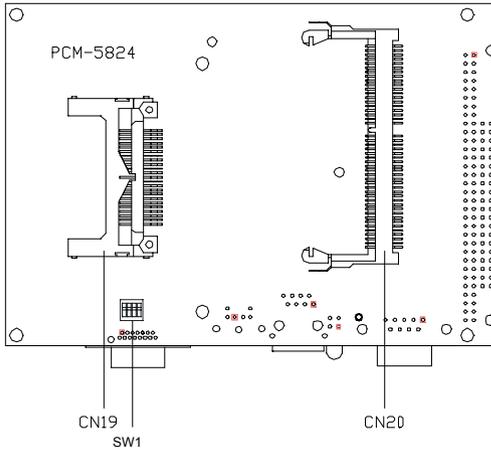
Figure 2-3: Connectors - solder side

2.4 Setting jumpers

2.4.1 Introduction

You may configure your card to match the needs of your application by setting jumpers. A jumper is the simplest kind of electrical switch. It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To "close" a jumper, you connect the pins with the clip. To "open" a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2, and 3. In this case you would connect either pins 1 and 2 or 2 and 3.

Open   **Closed**   **Closed 2-3**
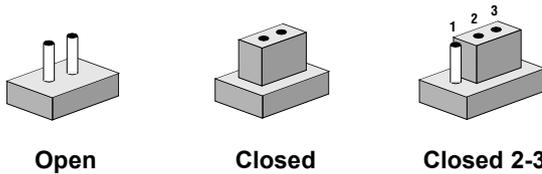
The jumper settings are schematically depicted in this manual as follows:

Open   **Closed**   **Closed 2-3**

A pair of needle-nose pliers may be helpful when working with jumpers.

If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any changes.

Generally, you simply need a standard cable to make most connections.

2.4.2 Settings details

J2:

Clear CMOS	
Closed Pins	**Result**
*1 - 2	3 V battery on
2 - 3	Clear CMOS

J3:

Watchdog timer	
Closed Pins	**Result**
*1 - 2	Reset
2 - 3	IRQ11

J4:

COM2 selector	
Closed pins	**Result**
*1 - 2	RS-232
3 - 4	RS-422
5 - 6	RS-485

J5:

Buzzer	
Pins	**Result**
*closed	Enabled
open	Disabled

J6:

LCD power	
Closed pins	**Voltage**
*1 - 2	5 V
2 - 3	3.3 V

* default setting

SW1:   Panel type Select

Panel #	Pin				Panel Type	
	1	2	3	4		
1	0	0	0	0	1024 x 768	Dual Scan STN Color Panel
2	x	0	0	0	1280 x 1024	TFT Color Panel
3	0	x	0	0	640 x 480	Dual Scan STN Color Panel
4	x	x	0	0	800 x 600	Dual Scan STN Color Panel
5	0	0	x	0	640 x 480	Sharp TFT Color Panel
6	x	0	x	0	640 x 480	18-bit TFT Color Panel
7	0	x	x	0	1024 x 768*	TFT Color Panel
8	x	x	x	0	800 x 600	TFT Color Panel
9	0	0	0	x	800 x 600	TFT Color Panel
10	x	0	0	x	800 x 600	TFT Color Panel
11	0	x	0	x	800 x 600	Dual Scan STN Color Panel
12	x	x	0	x	800 x 600	Dual Scan STN Color Panel
13	0	0	x	x	1024 x 768	TFT Color Panel
14	x	0	x	x	1280 x 1024	Dual Scan STN Color Panel
15	0	x	x	x	1024 x 600	Dual Scan STN Color Panel
16	x	x	x	x	1024 x 600	TFT Color Panel

* Default setting

Note: 1. Panel **#8,9,10** have different parameters in the settings, but all support 600 x 800 TFT LCD panel. **If you choose one, and it does not work, try another!**

2. Same condition as 1 for **#11,12** (600 x 800 DSTN LCD Panels)

3. Same as 1 and 2 for **#7, 13** (1024 x 768 TFT LCD)

2.5   Safety precautions

Warning!   *Always completely disconnect the power cord*
from your board whenever you are working on it.
Do not make connections while the power is on
because sensitive electronic components can be
damaged by the sudden rush of power.

Caution!   *Always ground yourself to remove any static*
charge before touching the board. Modern
electronic devices are very sensitive to static
electric charges. Use a grounding wrist strap at
all times. Place all electronic components on a
static-dissipative surface or in a static-shielded
bag when they are not in the chassis.

2.6 Installing DRAM (SODIMMs)

2.6.1 Introduction

You can install anywhere from 16 MB to 128 MB of on-board DRAM memory using 16, 32, 64 or 128 MB 144-pin SODIMMs (Small Outline Dual In-line Memory Modules).

2.6.2 Installing SODIMMs

Note: *The modules can only fit into a socket one way and their gold pins must point down into the SODIMM socket.*

The procedure for installing SODIMMs appears below. Please follow these steps carefully.

1. Ensure that all power supplies to the system are switched Off.

2. Install the SODIMM card. Install the SODIMM so that its gold pins point down into the SODIMM socket.

3. Slip the SODIMM into the socket at a 45 degree angle and carefully fit the bottom of the card against the connectors.

4. Gently push the SODIMM into a perpendicular position until the clips on the ends of the SODIMM sockets snap into place.

5. Check to ensure that the SODIMM is correctly seated and all connector contacts touch. The SODIMM should not move around in its socket.

2.7 IDE hard drive connector (CN11)

The built-in Enhanced IDE (Integrated Device Electronics) controller supports up to two IDE devices, including CD-ROM drives, tape backup drives, a large hard disk drive and other IDE devices. It also supports faster data transfer, PIO mode 3, mode 4, and Ultra DMA 33 mode.

2.7.1 Connecting the hard drive

Connecting drives is done in a daisy-chain fashion and requires one or two cables, depending on the drive size. All required cables are included in your PCM-5824 package. 1.8" and 2.5" drives need a 1 x 44-pin to 2 x 44-pin flat-cable connector. 3.5" drives use a 1 x 44-pin to 2 x 40-pin connector. However, the required connectors are not included in the PCM-5824 package.

Wire number 1 on the cable is red or blue, and the other wires are gray.

1. Connect one end of the cable to CN11. Make sure that the red (or blue) wire corresponds to pin 1 on the connector, which is labeled on the board (on the right side).

2. Plug the other end of the cable to the Enhanced IDE hard drive, with pin 1 on the cable corresponding to pin 1 on the hard drive. (See your hard drive's documentation for the location of the connector.)

Connect a second drive as described above.

Unlike floppy drives, IDE hard drives can connect to either end of the cable. If you install two drives, you will need to set one as the master and one as the slave by using jumpers on the drives. If you install just one drive, set it as the master.

2.8 CompactFlash™ disk (CN19)

The PCM-5824 is equipped with a CompactFlash disk socket on the solder side and it supports the IDE interface CompactFlash disk card. The socket itself is especially designed to prevent any incorrect installation of the CompactFlash disk card. When installing or removing the CompactFlash disk card, please make sure that the system power is off.

The CompactFlash disk card is defaulted as the E: disk drive in your PC system.

2.9 Floppy drive connector (CN2)

You can attach up to two floppy drives to the the PCM-5824 on-board controller. Any combination of 5¼" (360 KB and 1.2 MB) and/or 3½" (720 KB, 1.44 MB, and 2.88 MB) drives is possible.

A 34-pin daisy-chain drive connector cable is required for a dual-drive system. A 34-pin flat-cable connector is fitted on one end of the cable while the other end sports two sets of floppy disk drive connectors. Each set consists of a 34-pin flat-cable connector (for the 3½" drives) and a printed-circuit board connector (for the 5¼" drives).

2.9.1 Connecting the floppy drive

1. Plug in the 34-pin flat-cable connector into CN2. Make sure that the red wire corresponds to pin 1 on the connector.

2. Attach the appropriate conector at the other end of the cable to the floppy drive(s). You can use only one connector in the set. The set at the other end (after the twist in the cable) connects to the A: drive. The set in the middle connects to the B: drive.

3. If you are connecting a 5¼" floppy drive, line up the slot in the printed circuit board with the blocked-off part of the cable connector.

When connecting a 3½" floppy drive, you may have some

difficulties in determining which pin is pin number one. Look for a number on the circuit board indicating pin number one. In addition, you should check if the connector on the floppy drive has an extra slot. If the slot is up, pin number one should be on the right. Please refer to any documentation that came with the drive for more information.

If needed, connect the B: drive to the connectors in the middle of the cable as described as above.

If your cable needs to be custom made, you can find the pin assignments for the board's connector in Appendix A.

2.10 Parallel port connector (CN3)

Normally, the parallel port is used to connect the card to a printer. The PCM-5824 includes a multi-mode (ECP/EPP/SPP) parallel port, accessed through CN3 - a 26-pin flat-cable connector.

You will need an adapter cable if you use a traditional DB-25 connector. The adpater cable should have a 26-pin connector on one end and a 25-DB connector on the other.

The parallel port is designated as LPT1 and can be disabled or changed to LPT2 or LPT3 in the system BIOS setup.

The parallel port interrupt channel is designated as IRQ7.

The proper ECP/EPP DMA channel can be selected via the BIOS setup.

2.11 Keyboard and PS/2 mouse connector (CN17)

The PCM-5824 board provides a mini-DIN keyboard connector, which supports both a keyboard and a PS/2 style mouse. In most cases, especially in embedded applications, a keyboard is not used. If the keyboard is not present, the standard PC/AT BIOS will report an error or failure during the power-on self test (POST) after resetting the PC. The PCM-5824 board's BIOS standard setup menu allows you to select "All, But Keyboard" under the "Halt On" selection. This allows non-keyboard operation in embedded system applications without the system halting during the POST.

2.12 Power connectors

2.12.1 Main power connector +5 V, +12 V (CN9)

Supplies main power to the PCM-5824 (+5 V) and devices that require +12 V.

2.12.2 Auxilary power connector (CN10)

Supplies secondary power to peripherals that require -5 V and -12 V.

2.12.3 CPU fan power connector (CN6)

This connector is reserved for an optional fan, which facilitates a better working environment for the CPU.

2.13 IR connector (CN7)

The PCM-5824 provides an IrDA port for transfer rates of 115 kbps. This connector supports the optional wireless infrared transmitting and receiving module, which is mounted on the system case. Configuration of the module is done through BIOS setup.

2.14 Audio interfaces (CN5, CN1)

The PCM-5824 is equipped with a high quality audio interface, which provides 16-bit CD quality recording and playback as well as OPL3 compatible FM music. It is supported by all major operating systems and is completely compatible with Sound Blaster Pro.

2.14.1 Audio connector (CN5)

The PCM-5824 provides all major signals on a 16-pin flat-cable connector (CN5). These audio signals include Microphone in (mono), Line in (stereo) and Speaker out (stereo). An adapter cable is needed if traditional telephone jack connectors are used for these audio signals.

2.14.2 CD audio input connector (CN1)

All CD-ROM drives can provide analog audio signal output when used as a music CD player. The CN1 is a connector to input CD audio signals into the audio controller. The audio cable of your CD-ROM drive is suitable for connection to CN1.

2.14.3 Audio power source setting (J1)

The PCM-5824 is designed to work with a single +5 V power supply as audio interfaces usually function under +5 V. However, most audio controllers require an independant +12 V power source since this avoids noise interference from other digital circuits. By using J1, the PCM-5824 audio interface can also accept +12 V power sources for improved audio quality.

Table 2-3: Audio power source setting

	* +5 V	+12 V
J1		
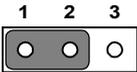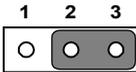
* default setting

Configuration of the audio interface is done completely via software utilities.You don't have to set any jumpers. For furhter information, please refer to Chapter 6 for audio setup details.

2.15 Serial ports (CN16, CN14)

The PCM-5824 offers two serial ports: one RS-232 and one RS-232/422/485. These ports allow you to connect to any serial device (a mouse, printers, etc.) or communication network.

2.15.1 COM1 RS-232 port (CN16)

The serial port connectors are mounted on the bottom edge of the card. The 9-pin D-SUB connector to the left of the card is the RS-232 port.

2.15.2 COM2 RS-232/422/485 selection (CN14)

The secondary port located above COM1, consists of a 14-pin, dual-in-line, male header and can be configured to operate in RS-232, RS-422, or RS-485 mode. This is done via J4.

Table 2-4: COM2 selection (J4)

	*RS-232	RS-422	RS-485
J4			

* default setting

The IRQ and address range for both ports are fixed. However, if you wish to disable the port or change these parameters later, you can do this in the system BIOS setup. The table below shows the settings for the PCM-5820 Series board's ports.

Table 2-5: Serial port default settings

Port	Address	Interrupt	Default
COM1	3E8, 3F8	IRQ4	3F8
COM2	2E8, 2F8	IRQ3	2F8

2.16 VGA interface connections

The PCM-5824 board's SVGA interface can facilitates conventional CRT displays as well as active LCD displays. The card has two connectors to support these displays, one for standard CRT VGA monitors and one for flat panel displays.

2.16.1 CRT display connector (CN18)

CN18 is a 15-pin, D-SUB connector commonly used for conventional CRT displays.

Detailed information on pin assignments for CRT display connector CN18 is given in Appendix A.

2.16.2 Flat panel display connector (CN21)

CN21 consists of a 44-pin, dual-inline header which can support a 24-bit LCD panel. Power supply (+12 V) present on CN21 depends on the supply connected to the board.

The PCM-5824 provides a bias control signal on CN21 that can be used to control the LCD bias voltage. It is recommended that the LCD bias voltage not be applied to the panel until the logic supply voltage (+5 V or +3.3 V) and panel video signals are stable. Under normal operation, the control signal (ENAVEE) is active high. When the PCM-5824's power is applied, the control signal is low until just after the relevant flat panel signals are present.

2.16.3 Extension flat panel connector (CN22)

The PCM-5824 supports a 36-bit LCD panel which must be connected to both the CN21 (44-pin) and the CN22 (16-pin). The pin assignments for both CN21 and the CN22 can be found in Appendix B.

2.16.4 LCD power setting (J6)

The PCM-5824 PCI SVGA interface supports 5 V and 3.3 V LCD displays. By changing the setting of J6, you can select the panel video signal level to be 5 V or 3.3 V.

Table 2-6: LCD power setting

	*5 V	3.3 V
J6	1 2 3	1 2 3
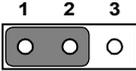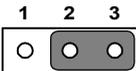
* default setting

Configuration of the LCD type is done completely via the software utility. You do not have to set any jumpers. Refer to Chapter 3 for software setup details.

Refer to Chapter 3 for details on connecting the two standard LCDs: Toshiba LTM10C042 and LTM 12C275A.

2.17 Ethernet interface connections

The PCM-5824 is equipped with a high performance 32-bit PCI Ethernet interface which is fully compliant with IEEE 802.3u 10/100 Mbps CSMA/CD standards.

2.17.1 100Base-T RJ-45 connector (CN13)

100Base-T connections are made via the on-board RJ-45 connector.

2.17.2 Ethernet power select (J7)

PCM-5824 supports (WOL) wake up on LAN function, to activate this function, select "standby 5V" mode on J7.

Table 2-7: Ethernet power select

	*+5 V	Standby 5 V
J3	1 2 3 ○ ● ●	1 2 3 ● ● ○
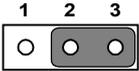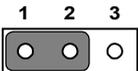
* default setting

2.18 Ethernet LED and HDD/power LEDs

2.18.1 LED1 (Ethernet LED)

Table 2-8: Ethernet LED setup

LED	Setting
Green lamp	Tx
Yellow lamp	Link

The LED should be set so that when the cable is connected, the yellow lamp is activated; and when data is transmitted, the green lamp is activated.

2.18.2 LED2 (Power and HDD LED)

Table 2-9: HDD/power LED setup

LED	Setting
Green lamp	Power
Yellow lamp	HDD

The LED should be set so when the HDD is accessed the yellow lamp is activated, and when the power is on the green lamp is activated.

2.19 Watchdog timer configuration

An on-board watchdog timer reduces the chance of disruptions which EMP (electro-magnetic pulse) interference can cause. This is an invaluable protective device for standalone or unmanned applications. Setup involves one jumper and running the control software (refer to Appendix C).

2.19.1 Watchdog timer action (J3)

When the watchdog timer activates (CPU processing has come to a halt), it can reset the system or generate an interrupt on IRQ11. This can be set via J3 as shown below:

Table 2-10: Watchdog timer action

	*System reset	IRQ11
J3	1 2 3 ⊙ ⊙ ○	1 2 3 ○ ⊙ ⊙
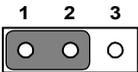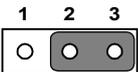
* default setting

2.20 USB connectors (CN4)

The PCM-5824 board provides two USB (Universal Serial Bus) interfaces which gives complete Plug and Play, and hot swaps for up to 127 external devices. The USB interfaces comply with USB specification Rev. 1.0 and are fuse protected.

The USB interfaces are accessed through two 10-pin flat-cable connectors, CN4. You will need an adapter cable if you use a standard USB connector.

The USB interfaces can be disabled in the system BIOS setup.

Software Configuration

This chapter details the software configuration information. It shows you how to configure the card to match your application requirements. Award system BIOS is covered in Chapter 4.

Sections include:

• LCD display configuration

• Connections for two standard LCDs

3.1 Introduction

The PCM-5824 system BIOS and custom drivers are located in a 256 KB, 32-pin Flash ROM device, designated U14. A single Flash chip holds the system BIOS and VGA BIOS. The display type can be configured via software. This method minimizes the number of chips and eases configuration. You can change the display BIOS simply by reprogramming the Flash chip.

3.2 Utility CD disk

The PCM-5824 is supplied with a software utility on CD-ROM. This disk contains the necessary file for setting up the VGA display. Directories and files on the disk are as follows:

```
┌──────── AWDFLASH.EXE
├──────── CBROM.EXE
├──────── RSET8139.EXE
└──────── 5824Vxxx.BIN
```

Figure 3-1: Contents of the PCM-5824 utility disk

AWDFLASH.EXE
This program allows you to update the BIOS Flash ROM.

5820V110.BIN
This binary file contains the system BIOS.

CBROM.EXE
This program allows you to combine your own VGA BIOS with system BIOS (5824V110.BIN).

RSET8139.EXE

This program enables you to view the current Ethernet configuration, reconfigure the Ethernet interface (medium type, etc.), and execute useful diagnostic functions.

3.3 VGA display software configuration

The PCM-5824 on-board VGA/LCD interface supports an 18-bit TFT LCD, flat panel displays and traditional analog CRT monitors. The interface can drive CRT displays with resolutions up to 1024 x 768 in 16 bpp. It is also capable of driving color panel displays with resolutions of 1024 x 768 in 18 bpp. The LCD type is configured completely via the software utility, so you do not have to set any jumpers. Configure the LCD type as follows:

1. Apply power to the PCM-5824 with a color TFT display attached. This is the default setting for the PCM-5824. Make sure that the AWDFLASH.EXE and *.BIN files are located in the working drive.

Note: *Make sure that you do not run AWDFLASH.EXE while your system is operating in EMM386 mode.*

2. At the prompt, type AWDFLASH.EXE and press <Enter>. The VGA configuration program will then display the following:
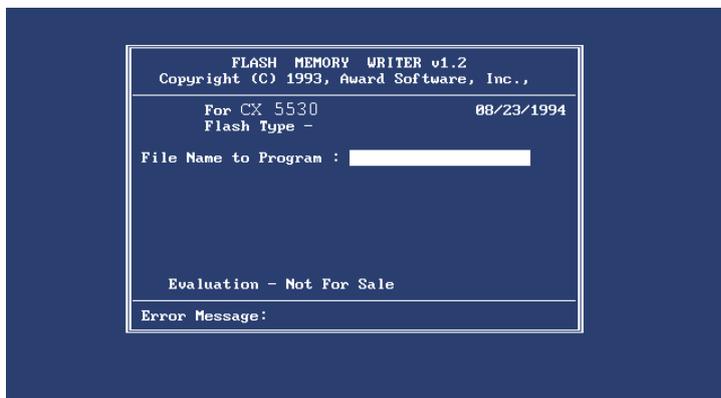
Figure 3-2: BIOS VGA setup screen

3. At the prompt, type in the BIN file which supports your display. When you are sure that you have entered the file name correctly press <Enter>. The screen will ask "Do you want to save?" If you wish to continue press Y. If you change your mind or have made a mistake press N.

4. If you decide to continue, the screen will issue a prompt which will then ask "Are you sure to program (Y/N)?" If you wish to continue, press Y. Press N to exit the program.

The new VGA configuration will then write to the ROM BIOS chip. This configuration will remain the same until you run the AWDFLASH.EXE program and change the settings.

3.4 Connections for five standard LCDs

3.4.1 Connections to Sharp LM64183P, LM64P89 (640 x 480 DSTN MONO LCD)

Table 3-1: Connections to Sharp LM64183P & LM64P89

LM64183/64P89		PCM-5824 CN21	
Pin	Name	Pin	Name
CN1-1	S	36	FLM
CN1-2	CP1	38	LP
CN1-3	CP2	35	SHFCLK
CN1-4	DISP	5	+5 V
CN1-5	VDD	6	+5 V
CN1-6	VSS	3	GND
CN1-7	VEE	-	External power*
CN1-8	DU0	12	P3
CN1-9	DU1	11	P2
CN1-10	DU2	10	P1
CN1-11	DU3	9	P0
CN1-12	DL0	16	P7
CN1-13	DL1	15	P6
CN1-14	DL2	14	P5
CN1-15	DL3	13	P4

* LM64183P   -17 V   LM64P89   -20 V

Note: Standard BIOS supports TFT and DSTN LCD panels.

3.4.2 Connections to PLANAR EL
(640 x 480 AD4 EL)

Table 3-2: Connections to PLANAR EL

PLANAR 640 x 480 AD4		PCM-5824 CN21	
Pin	Name	Pin	Name
1	GND	3	GND
2	DO	21	P12
3	GND	3	GND
4	D1	22	P13
5	GND	3	GND
6	D2	23	P14
7	NC	—	—
8	D3	24	P15
9	NC	—	—
10	D4	17	P8
11	NC	—	—
12	D5	18	P9
13	NC	—	—
14	D6	19	P10
15	GND	4	GND
16	D7	20	P11
17	GND	4	GND
18	VCLK	42	ASHFCLK
19	GND	4	GND
20	/BLANK	—	—
21	GND	8	GND
22	HS	37	M
23	NC	—	—
24	VS	36	FLM
25	NC	—	—
26	SELFTST	39	GND
27	COLMAP	39	GND
28	ENABLE	—	—
29	RESERVED	—	—
30	/LOWPOW	—	—
31,32	NC	—	—
33	RESERVED	—	—
34	NC	—	—

3.4.3 Connections to Toshiba LTM10C042 (640 x 480 TFT color LCD)

Table 3-3: Connections to Toshiba LTM10DC042

LTM10C042		PCM-5824 CN21	
Pin	**Name**	**Pin**	**Name**
1	GND	3	GND
2	CLK	35	SHFCLK
3	GND	4	GND
4	R0	27	P18
5	R1	28	P19
6	R2	29	P20
7	GND	8	GND
8	R3	30	P21
9	R4	31	P22
10	R5	32	P23
11	GND	33	GND
12	G0	19	P10
13	G1	20	P11
14	G2	21	P12
15	GND	33	GND
16	G3	22	P13
17	G4	23	P14
18	G5	24	P15
19	GND	34	GND
20	ENAB	37	M
21	GND	34	GND
22	B0	11	P2
23	B1	12	P3
24	B2	13	P4
25	GND	39	GND
26	B3	14	P5
27	B4	15	P6
28	B5	16	P7
29	GND	39	GND
30	VDD	5	+5 V
31,32	VDD	6	+5 V

3.4.4 Connections to Sharp LM64C142
(640 x 480 DSTN color LCD)

Table 3-4: Connections to Sharp LM64C142

LM64C142 Pin	Name	Pin	PCM-5824 CN21 Name
CN1-1	YD	36	FLM
CN1-2	LP	38	LP
CN1-3	XCX	35	SHFCLK
CN1-4	DISP	5	+5 V
CN1-5	VDD	6	+5 V
CN1-6	VSS	3	GND
CN1-7	VEE	—	+27*
CN1-8	DU0	20	P11
CN1-9	DU1	19	P10
CN1-10	DU2	18	P9
CN1-11	DU3	17	P8
CN1-12	DU4	12	P3
CN1-13	DU5	11	P2
CN1-14	DU6	10	P1
CN1-15	DU7	9	P0
CN2-1	VSS	4	GND
CN2-2	DL0	24	P15
CN2-3	DL1	23	P14
CN2-4	DL2	22	P13
CN2-5	DL3	21	P12
CN2-6	DL4	16	P7
CN2-7	DL5	15	P6
CN2-8	DL6	14	P5
CN2-9	DL7	13	P4
CN2-10	VSS	8	GND

3.4.5 Connections to Toshiba LTM12C275A (800 x 600 TFT color LCD)

Table 3-5: Connections to Toshiba LTM12C2775A

LTM12C275A		PCM-5824 CN21	
Pin	Name	Pin	Name
1	GND	3	GND
2	NCLK	35	SHFCLK
3	NC	-	NC
4	NC	-	NC
5	GND	4	GND
6	R0	27	P18
7	R1	28	P19
8	R2	29	P20
9	R3	30	P21
10	R4	31	P22
11	R5	32	P23
12	GND	8	GND
13	G0	19	P10
14	G1	20	P11
15	G2	21	P12
16	G3	22	P13
17	G4	23	P14
18	G5	24	P15
19	GND	33	GND
20	B0	11	P2
21	B1	12	P3
22	B2	13	P4
23	B3	14	P5
24	B4	15	P6
25	B5	16	P7
26	ENAB	37	M/DE
27	GND	34	GND
28	VCC	5	+5 V
29	VCC	6	+5 V
30	GND	39	GND

3.5 Ethernet interface configuration

The PCM-5824 on-board Ethernet interface supports all major network operating systems. To configure the medium type, to view the current configuration, or to run diagnostics, do the following:

1. Power the PCM-5824 on. Make sure that the RSET8139.EXE file is located in the working drive.

2. At the prompt, type RSET8139.EXE and press <Enter>. The Ethernet configuration program will then be displayed.

3. This simple screen shows all the available options for the Ethernet interface. Just highlight the option you wish to change by using the Up and Down keys. To change a selected item, press <Enter>, and a screen will appear with the available options. Highlight your option and press <Enter>. Each highlighted option has a helpful message guide displayed at the bottom of the screen for additional information.

4. After you have made your selections and are sure this is the configuration you want, press ESC. A prompt will appear asking if you want to save the configuration. Press Y if you want to save.

The Ethernet Setup Menu also offers three very useful diagnostic functions. These are:

1. Run EEPROM test

2. Run Diagnostics on Board

3. Run Diagnostics on Network

Each option has its own display screen that shows the format and result of any diagnostic tests undertaken.

Award BIOS Setup

This chapter describes how to set BIOS configuration data.

4.1 System test and initialization

These routines test and initialize board hardware. If the routines encounter an error during the tests, you will either hear a few short beeps or see an error message on the screen. There are two kinds of errors: fatal and non-fatal. The system can usually continue the boot up sequence with non-fatal errors. Non-fatal error messages usually appear on the screen along with the following instructions:

```
press <F1> to RESUME
```

Write down the message and press the F1 key to continue the bootup sequence.

4.1.1 System configuration verification

These routines check the current system configuration against the values stored in the board's CMOS memory. If they do not match, the program outputs an error message. You will then need to run the BIOS setup program to set the configuration information in memory.

There are three situations in which you will need to change the CMOS settings:

1. You are starting your system for the first time

2. You have changed the hardware attached to your system

3. The CMOS memory has lost power and the configuration information has been erased.

The PCM-5824 CMOS memory has an integral lithium battery backup. The battery backup should last ten years in normal service, but when it finally runs down, you will need to replace the complete unit.

4.2 Award BIOS setup

Award's BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backed CMOS RAM so that it retains the Setup information when the power is turned off.

4.2.1 Entering setup

Power on the computer and press <Del> immediately. This will allow you to enter Setup.

```
                      ROM PCI/ISA BIOS (2A434AKC)
                         CMOS SETUP UTILITY
                        AWARD SOFTWARE, INC.

    STANDARD CMOS SETUP            INTEGRATED PERIPHERALS

    BIOS FEATURES SETUP            PASSWORD SETTING

    CHIPSET FEATURES SETUP         IDE HDD AUTO DETECTION

    POWER MANAGEMENT SETUP         SAVE & EXIT SETUP

    PNP/PCI CONFIGURATION          EXIT WITHOUT SAVING

    LOAD SETUP DEFAULTS


    Esc : Quit                    ↑ ↓ → ←   : Select Item
    F10 : Save & Exit Setup       (Shift)F2 : Change Color
```
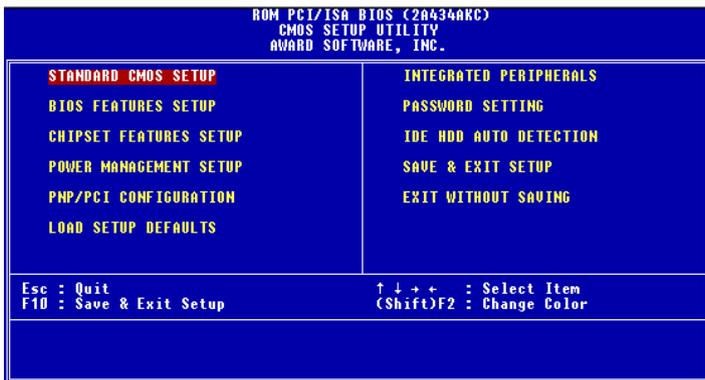
Figure 4-1: BIOS setup program initial screen

4.2.2 Standard CMOS setup

When you choose the STANDARD CMOS SETUP option from the INITIAL SETUP SCREEN menu**,** the screen shown below is displayed. This standard Setup Menu allows users to configure system components such as date, time, hard disk drive, floppy drive and display. Once a field is highlighted, on-line help information is displayed in the left bottom of the Menu screen.

```
                    ROM PCI/ISA BIOS (2A434AKC)
                        STANDARD CMOS SETUP
                        AWARD SOFTWARE, INC.

   Date (mm:dd:yy) : Tue, Jan  2 2001
   Time (hh:mm:ss) : 10 : 34 : 15

   HARD DISKS          TYPE    SIZE    CYLS HEAD PRECOMP LANDZ SECTOR  MODE

   Primary Master   :    0       0      0    0      0     0      0 NORMAL
   Primary Slave    :    0       0      0    0      0     0      0 NORMAL
   Secondary Master :    0       0      0    0      0     0      0 NORMAL


   Drive A : None
   Drive B : None

   Video   : EGA/VGA
   Halt On : All Errors

 ESC : Quit              ↑ ↓ → ←   : Select Item     PU/PD/+/- : Modify
 F1  : Help              (Shift)F2 : Change Color
```

Figure 4-2: CMOS setup screen

4.2.3 BIOS features setup

By choosing the BIOS FEATURES SETUP option from the INITIAL
SETUP SCREEN menu, the screen below is displayed. This sample
screen contains the manufacturer's default values for the PCM-5820
Series.

```
                    ROM PCI/ISA BIOS (2A434AKC)
                        BIOS FEATURES SETUP
                        AWARD SOFTWARE, INC.

Virus Warning            : Disabled    Video  BIOS Shadow  : Enabled
CPU Internal Cache       : Enabled     C8000-CBFFF Shadow  : Disabled
                                       CC000-CFFFF Shadow  : Disabled
Quick Power On Self Test : Enabled     D0000-D3FFF Shadow  : Disabled
Boot From LAN First      : Disabled    D4000-D7FFF Shadow  : Disabled
Boot Sequence            : C,A,SCSI    D8000-DBFFF Shadow  : Disabled
Swap Floppy Drive        : Disabled    DC000-DFFFF Shadow  : Disabled
Boot Up Floppy Seek      : Enabled
Boot Up NumLock Status   : On
Boot Up System Speed     : High
Gate A20 Option          : Fast
Memory Parity Check      : Enabled
Typematic Rate Setting   : Disabled
Typematic Rate (Chars/Sec) : 6
Typematic Delay (Msec)   : 250
Security Option          : Setup       ESC : Quit         ↑↓→← : Select Item
PCI/VGA Palette Snoop     : Disabled    F1  : Help         PU/PD/+/- : Modify
OS Select For DRAM > 64MB : Non-OS2     F5  : Old Values  (Shift)F2 : Color
Report No FDD For WIN 95  : Yes         F7  : Load Setup Defaults
```

Figure 4-3: BIOS features setup

4.2.4 Chipset features setup

By choosing the CHIPSET FEATURES SETUP option from the INITIAL SETUP SCREEN menu, the screen below is displayed. This sample screen contains the manufacturer's default values for the PCM-5824.
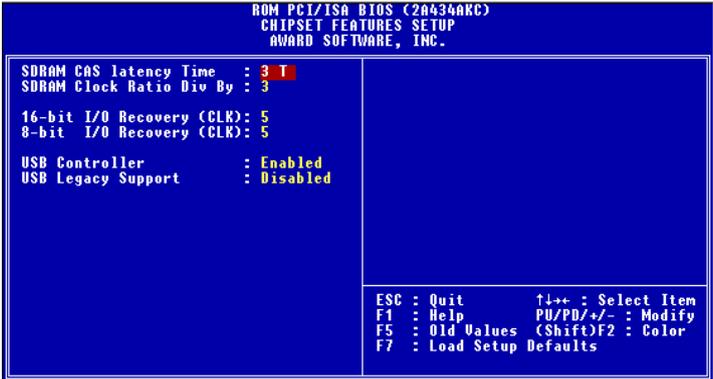
Figure 4-4: Chipset features setup

4.2.5 Power management setup

By choosing the POWER MANAGEMENT SETUP option from the INITIAL SETUP SCREEN menu, the screen below is displayed. This sample screen contains the manufacturer's default values for the PCM-5824.

```
              ROM PCI/ISA BIOS (2A434AKC)
                POWER MANAGEMENT SETUP
                 AWARD SOFTWARE, INC.

Power Management      : Disabled    IRQ1  (KeyBoard)    : ON
                                    IRQ3  (COM 2)       : OFF
    ** PM Timers **                 IRQ4  (COM 1)       : OFF
Doze Mode             : Disabled    IRQ5  (LPT 2)       : OFF
Standby Mode          : Disabled    IRQ6  (Floppy Disk): OFF
HDD Power Down        : Disabled    IRQ7  (LPT 1)       : OFF
MODEM Use IRQ         : NA          IRQ9  (IRQ2 Redir)  : OFF
                                    IRQ10 (Reserved)    : OFF
Throttle Duty Cycle   : 33.3 %      IRQ11 (Reserved)    : OFF
                                    IRQ12 (PS/2 Mouse)  : OFF
                                    IRQ13 (Coprocessor) : OFF
                                    IRQ14 (Hard Disk)   : OFF
                                    IRQ15 (Reserved)    : OFF


                                    ESC : Quit      ↑↓→← : Select Item
                                    F1  : Help      PU/PD/+/- : Modify
                                    F5  : Old Values  (Shift)F2 : Color
                                    F7  : Load Setup Defaults
```
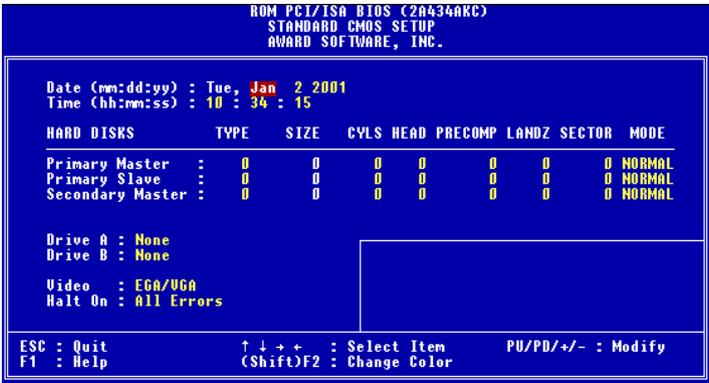
Figure 4-5: Power management setup

4.2.6 PnP/PCI configuration

By choosing the PnP/PCI CONFIGURATION option from the Initial Setup Screen menu, the screen below is displayed. This sample screen contains the manufacturer's default values for the PCM-5824.

```
              ROM PCI/ISA BIOS (2A434AKC)
                  PNP/PCI CONFIGURATION
                   AWARD SOFTWARE, INC.

PNP OS Installed        : No        PCI IRQ Actived By  : Level
Resources Controlled By : Auto
Reset Configuration Data : Disabled
                                    LAN Card Boot ROM   : Disabled




                                    ESC : Quit        ↑↓→← : Select Item
                                    F1  : Help        PU/PD/+/- : Modify
                                    F5  : Old Values  (Shift)F2 : Color
                                    F7  : Load Setup Defaults
```
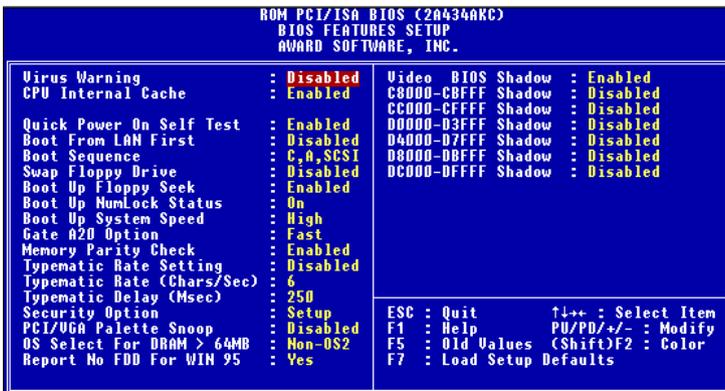
Figure 4-6: PnP/PCI configuration

4.2.7 Integrated peripherals

By choosing the INTEGRATED PERIPHERALS option from the INITIAL SETUP SCREEN menu, the screen below is displayed. This sample screen contains the manufacturer's default values for the PCM-5824. The PANEL TYPE by default supports a 18-bit 640 x 480 TFT LCD panel display.
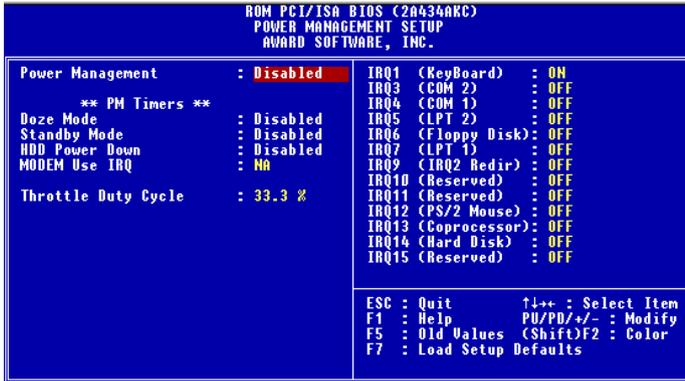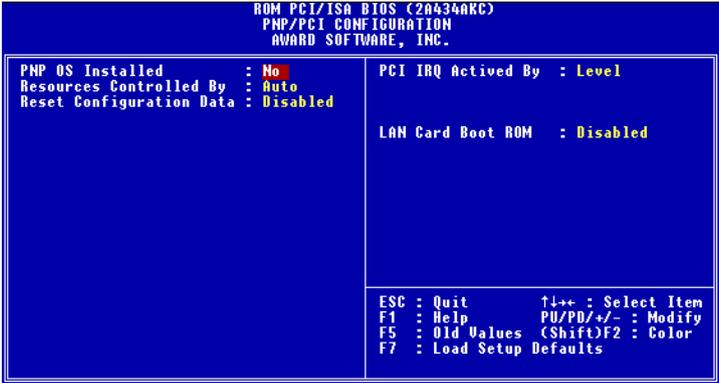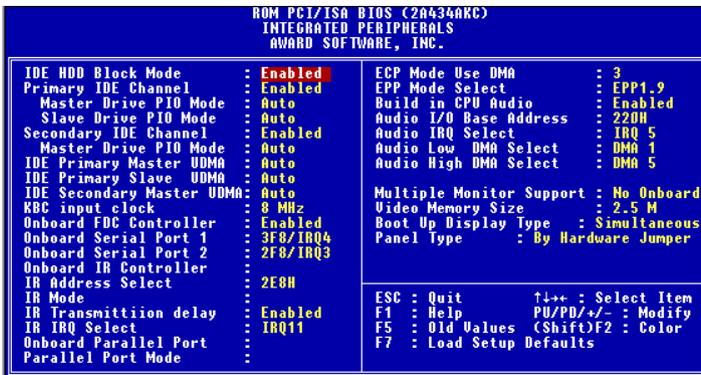
```
                    ROM PCI/ISA BIOS (2A434AKC)
                      INTEGRATED PERIPHERALS
                       AWARD SOFTWARE, INC.

 IDE HDD Block Mode      : Enabled    ECP Mode Use DMA          : 3
 Primary IDE Channel     : Enabled    EPP Mode Select           : EPP1.9
   Master Drive PIO Mode : Auto       Build in CPU Audio        : Enabled
   Slave Drive PIO Mode  : Auto       Audio I/O Base Address    : 220H
 Secondary IDE Channel   : Enabled    Audio IRQ Select          : IRQ 5
   Master Drive PIO Mode : Auto       Audio Low  DMA Select     : DMA 1
 IDE Primary Master UDMA : Auto       Audio High DMA Select     : DMA 5
 IDE Primary Slave  UDMA : Auto
 IDE Secondary Master UDMA: Auto      Multiple Monitor Support : No Onboard
 KBC input clock         : 8 MHz      Video Memory Size         : 2.5 M
 Onboard FDC Controller  : Enabled    Boot Up Display Type   : Simultaneous
 Onboard Serial Port 1   : 3F8/IRQ4   Panel Type      : By Hardware Jumper
 Onboard Serial Port 2   : 2F8/IRQ3
 Onboard IR Controller   :
 IR Address Select       : 2E8H
 IR Mode                 :            ESC : Quit        ↑↓←→ : Select Item
 IR Transmittiion delay  : Enabled    F1  : Help       PU/PD/+/- : Modify
 IR IRQ Select           : IRQ11      F5  : Old Values  (Shift)F2 : Color
 Onboard Parallel Port   :            F7  : Load Setup Defaults
 Parallel Port Mode      :
```

Figure 4-7: Integrated peripherals

4.2.8 Load BIOS defaults

LOAD BIOS DEFAULTS loads the default system values directly from ROM. If the stored record created by the Setup program becomes corrupted (and therefore unusable), these defaults will load automatically when you turn the PCM-5824 on.
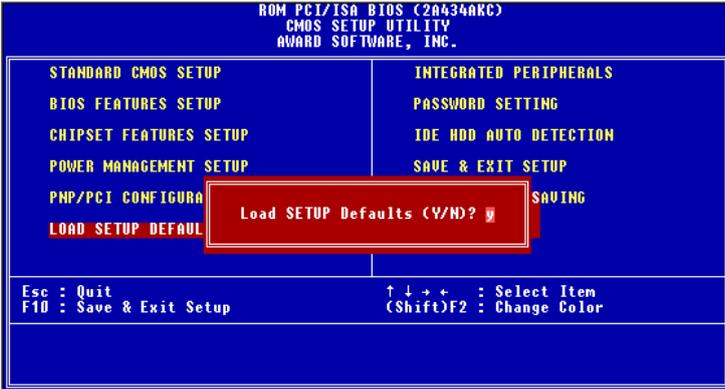

Figure 4-8: Load BIOS defaults screen

4.2.9 Change password

To change the password, choose the PASSWORD SETTING option form the Setup main menu and press <Enter>.

1. If the CMOS is bad or this option has never been used, a default password is stored in the ROM. The screen will display the following messages:

    <div style="background:black;color:white;padding:4px;text-align:center">Enter Password:</div>

    Press <Enter>.

2. If the CMOS is good or this option has been used to change the default password, the user is asked for the password stored in the CMOS. The screen will display the following message:

    <div style="background:black;color:white;padding:4px;text-align:center">Confirm Password:</div>
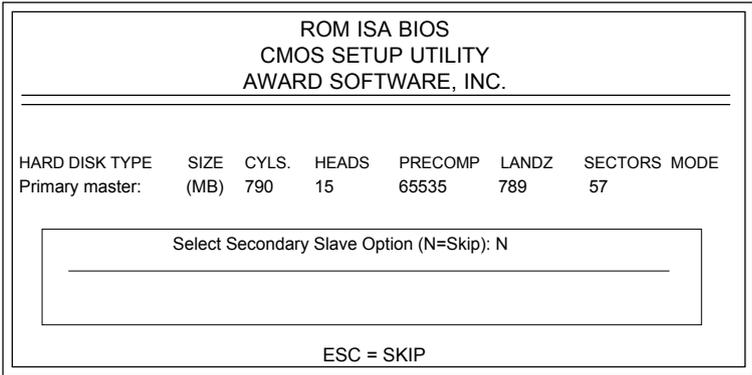
    Enter the current password and press <Enter>.

3. After pressing <Enter> (ROM password) or the current password (user-defined), you can change the password stored in the CMOS. The password can be at most eight (8) characters long.

Remember - to enable this feature, you must first select either Setup or System in the BIOS FEATURES SETUP.

4.2.10 Auto detect hard disk

The IDE HDD AUTO DETECTION utility can automatically detect the IDE hard disk installed in your system. You can use it to self-detect and/or correct the hard disk type configuration.

```
┌─────────────────────────────────────────────────────────────────┐
│                        ROM ISA BIOS                               │
│                     CMOS SETUP UTILITY                            │
│                    AWARD SOFTWARE, INC.                           │
│ ───────────────────────────────────────────────────────────────  │
│                                                                   │
│  HARD DISK TYPE    SIZE  CYLS.  HEADS   PRECOMP  LANDZ  SECTORS MODE│
│  Primary master:   (MB)  790    15      65535    789    57        │
│   ┌─────────────────────────────────────────────────────────┐    │
│   │      Select Secondary Slave Option (N=Skip): N          │    │
│   │  ─────────────────────────────────────────────────────  │    │
│   │                                                          │    │
│   └─────────────────────────────────────────────────────────┘    │
│                        ESC = SKIP                                 │
└─────────────────────────────────────────────────────────────────┘
```

Figure 4-9: IDE HDD auto detection screen

4.2.11 Save & exit setup

If you select this option and press <Enter>, the values entered in the setup utilities will be recorded in the chipset's CMOS memory. The microprocessor will check this every time you turn your system on and compare this to what it finds as it checks the system. This record is required for the system to operate.

4.2.12 Exit without saving

Selecting this option and pressing <Enter> lets you exit the Setup program without recording any new values or changing old ones.

PCI SVGA Setup

- Introduction
- Installation of SVGA driver for
  - Windows 3.1
  - Windows 95
  - Windows NT
- Further information

5.1 Introduction

The PCM-5824 has an on-board PCI flat panel/VGA interface. The specifications and features are described as follows:

5.1.1 Chipset

The PCM-5824 uses a C&T 6900/69030 chipset for its PCI/SVGA controller. It supports many popular LCD, EL, and gas plasma flat panel displays, aa well as conventional analog CRT monitors. The 6900/69030 VGA BIOS supports monochrome LCD, EL, color TFT and STN LCD flat panel displays. In addition, it also supports interlaced and non-interlaced analog monitors (color and monochrome VGA) in high-resolution modes, while maintaining complete IBM VGA compatibility. Digital monitors (i.e. MDA, CGA, and EGA) are NOT supported. Multiple frequency (multisync) monitors are handled as if they were analog monitors.

5.1.2 Display memory

With on-board 2 MB display memory, the VGA controller can drive CRT displays or color panel displays with resolutions up to 1024 x 768 @ 64 K colors. With C&T 69030, the display memory can be expanded to 4 MB for true-color resolution of 1024 x 768.

5.1.3 Display types

CRT and panel displays can be used simultaneously. The PCM-5824 can be set in one of three configurations: on a CRT, on a flat panel display, or on both simultaneously. The system is initially set to simultaneous display mode. The CD utility disc includes three *.COM files in the subdirectory **Biscuit\5824\Utility** which can be used to configure the display. In order to use these configuration programs, type the file name and path at the DOS prompt.

CT.COM: Enables CRT display only

FP.COM: Enables panel display only

SM.COM: Enables both displays simultaneously

5.2 Installation of SVGA driver

Complete the following steps to install the SVGA driver. Follow the procedures in the flow chart that apply to the operating system that you are using within your PCM-5824.

*Important:* *The following windows illustrations are examples only. You must follow the flow chart instructions and pay attention to the instructions which then appear on your screen.*

Note: *<Enter> means pressing the "Enter" key on the keyboard.*

5.2.1 Installation for Windows 95/98

1. Select "Start", "Settings", "Control Panel", "Display", "Settings". Then click on "Advanced Properties".
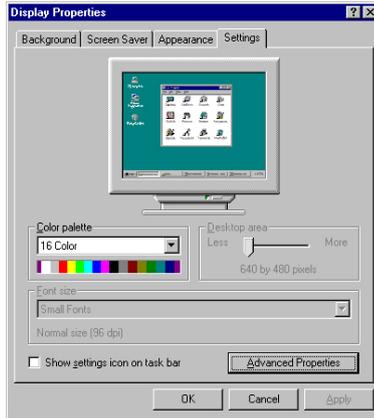
2. Choose the "Adaptor" label. Press "Change..."
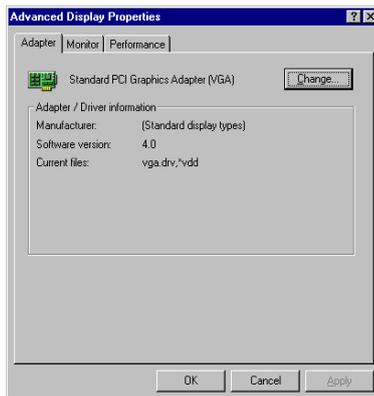
3. Press the "Have Disk" button.
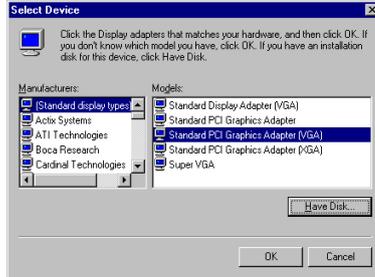
4. Insert the utility disk into the floppy disk drive.

Type: "D:\Biscuit\5824\VGA\69000\Win95"

Press "OK".

5. Select the highlighted item.

Click the "OK" button.
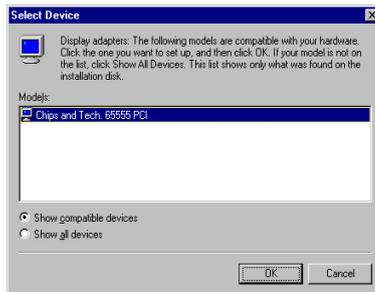
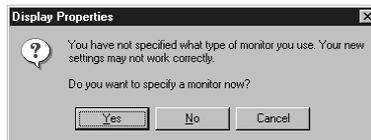6. C&T69000/69030 appears in the adapter label. Click on the "Apply" button.
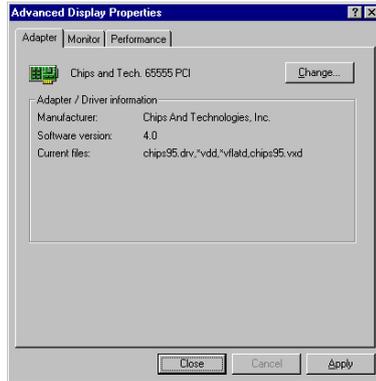
7. Press "Yes" to reboot.

8. Repeat Step 1. The "CHIPS" label appears in the "Display". Adjust resolution and color.
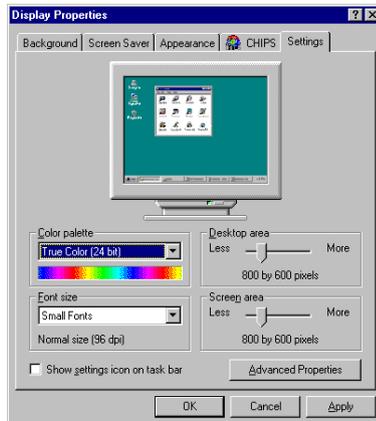
9. Click the "CHIPS" label. Adjust the refresh rate and display type. Press "OK" to exit.
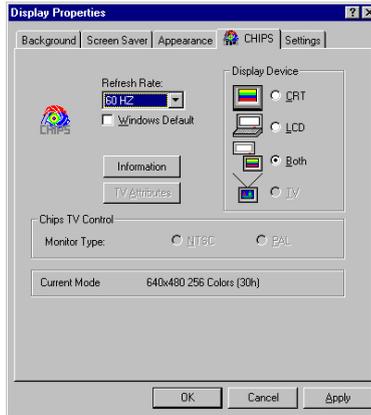
10. Press "Yes" to set the monitor type.

11. Select "Standard", "Super VGA 800 x 600". or "XGA". Then press the "OK" button.
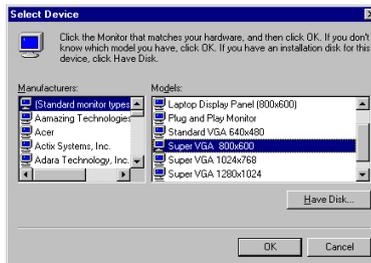
12. Choose "Restart" to reboot. End
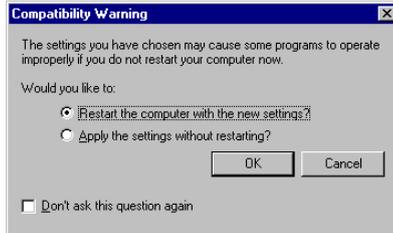
Note: 1. In step 4, for Windows 98, the path is:
             "D:\Biscuit\5824\VGA\69000\Win98\"

5.2.2 Installation for Windows 3.1

1. Insert the utility disk into the floppy disk drive.

Select "File" in the Program Manager.

Click "Run" and type:
"D:\Biscuit\5824\VGA\69000\Win31\Setup.exe".

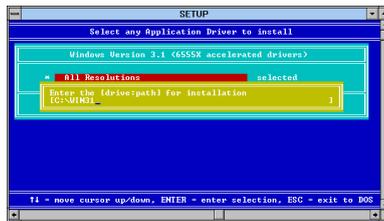2. Choose the language you want to use during installation.

3. Select the highlighted item. Press "ENTER".

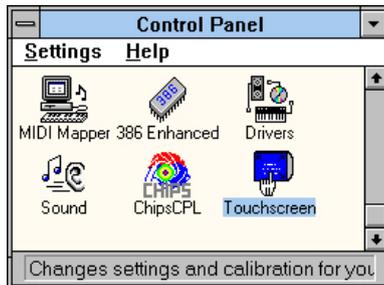4. Press "ENTER" to install all resolutions.
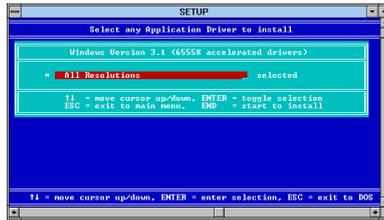
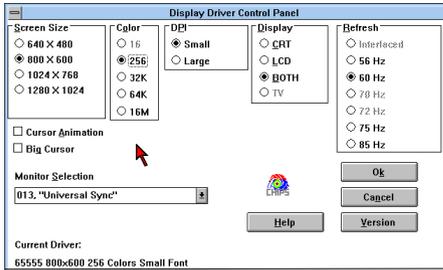5. Type the path of the operating system.

6. When installation is complete, reboot the system.

You will see the "ChipsCPL" icon in the control panel.

7. Double click "ChipsCPL".

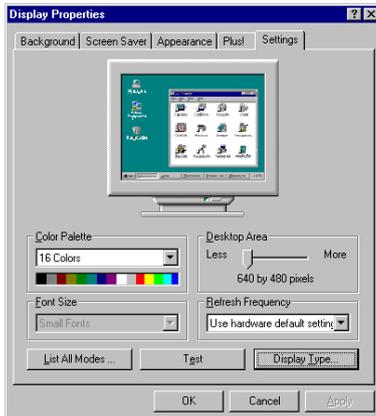Adjust the screen size, color and refresh rate to your preferences.
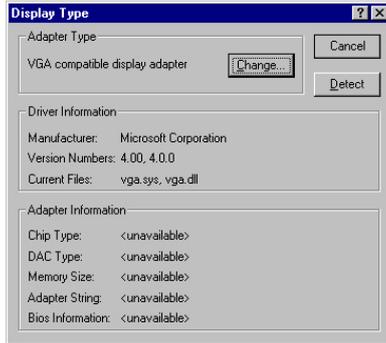
5.2.3 Installation for Windows NT

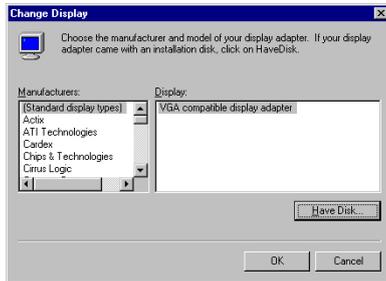1. Select "Start", "Settings", "Control Panel", then double click the "Display" icon.

2. Choose the "Settings" label. Then press the "Display Type" button.

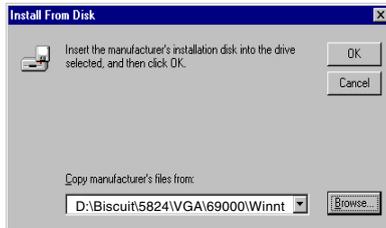3. Press the "Change...." button.

4. Click the 'Have Disk..." button.

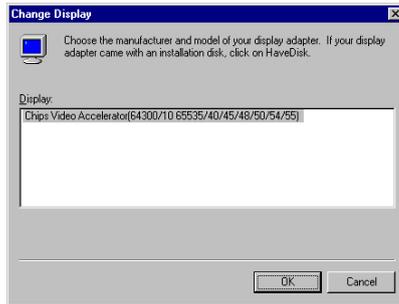5. Insert the utility disk into the floppy disk drive.

Type: "D:\Biscuit\5824\VGA\69000\Winnt"

Press the "OK" button.

6. Select the highlighted item.

Press the "OK" button.

7. Press "Yes" to proceed.

8. Press "OK" to reboot.

9. Repeat Step 1 to select the "Settings" label.

Adjust resolution and color.

Click "Test" to see the result.

Click "OK" to save the setting. End
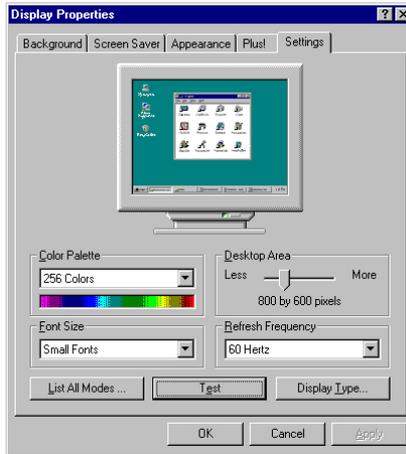
5.3 Further information

For further information about PCI/SVGA installation in your PCM-5824, including driver updates, troubleshooting guides and FAQ lists, visit the following web resources:

Asillion website: **www.asilian.com**

EMAC website: **www.emacinc.com**

Audio

- Introduction
- Installation of audio driver for Windows 95/98/NT

6.1   Introduction

The PCM-5824's on-board audio interface provides high-quality stereo sound and FM music synthesis (ESFM) by using the CX5530 audio controller from Cyrix Corporation. The audio interface can record, compress, and play back voice, sound, and music with a built-in mixer control. The PCM-5824's on-board audio interface also supports the Plug and Play (PnP) standard and provides PnP configuration for audio, FM, and MPU-104 logical devices. It is compatible with AC97 version 2.0, voice, and music functions. The ESFM synthesizer is register compatible with the OPL3 and has extended capabilities.

6.2   Installation of audio driver

Before installing the audio driver, please take note of the procedures detailed below. You must know which operating system you are using in your PCM-5824's, and then refer to the corresponding installation flow chart. Just follow the steps in the flow chart. You can quickly and successfully complete the installation, even though you are not familiar with instructions for Windows.

Note: *The CD-ROM drive is designated as "D" throughout this chapter.*

6.2.1 Installation for Windows 95/98

1.a. Select "Start", "Settings", "Control Panel", "System", "Device Manager".

b. Click on "Other Devices".

c. Remove items related to ESS 1869.
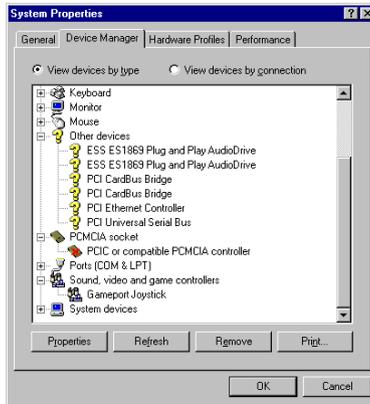
2.a. Select "Add new hardware".

b. Click "Next".

3.a. Choose "No", then click "Next".

4. a. Select "Sound Video..."

   b. Click "Next"

5.a. Click "Have Disk"

6. a. Insert the disc into the CD-ROM drive.

b. Type the correct path D: 5824\VGA\Win9X\Audio" and click the "OK" button.

7. a. Select "ES1869 Control interface"

8. a. Click "Finish" to complete.

9. Click "OK"

10. a. Insert Windows 9x CD.

   b. Type the path of your windows 9x disc and click "OK".
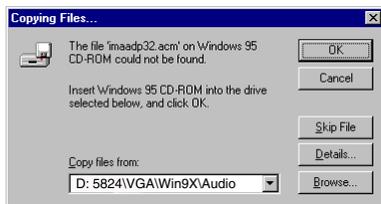
11. a. Click "Yes" to restart.

6.2.2 Installation for Windows NT

1. a. Select "Start", "Settings", "Control Panel".

   b. Double click "Multimedia".

2. a. Select "Devices" item.

   b. Click "add".
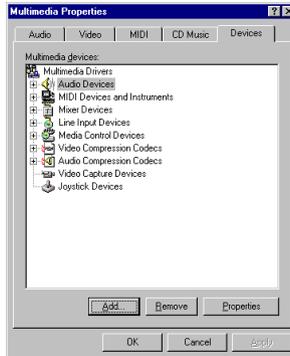
3. Select the "Unlisted" item.

   b. Click "Ok".

4. a. Insert the disc into the CD-ROM drive.

   b. Type the correct path D: Biscuit\5824\VGA\Winn\Audio" and click the "OK" button.

5. a. Choose the highlighted section then click "OK".
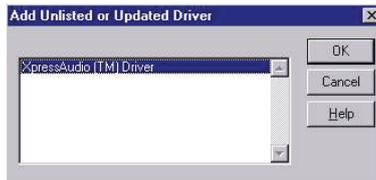
6. a. Set the I/O addresss.

   b. Click "Continue".

7. a. Set "Express Audio Configuration"

   b. Click "Ok" to restart.

PCI Bus Ethernet Interface

This chapter provides information on Ethernet configuration.

- Introduction

- Installation of Ethernet driver for Windows 95/98/NT

- Further information

7.1   Introduction

The PCM-5824 is equipped with a high performance 32-bit Ethernet chipset which is fully compliant with IEEE 802.3 100 Mbps CSMA/CD standards. It is supported by major network operating systems. It is also both 100Base-T and 10Base-T compatible. The medium type can be configured via the RSET8139.exe program included on the utility disk.

The Ethernet port provides a standard RJ-45 jack on board. The network boot feature can be utilized by incorporating the boot ROM image files for the appropriate network operating system. The boot ROM BIOS files are combined with system BIOS, which can be enabled/disabled in the BIOS setup.

7.2   Installation of Ethernet driver

Before installing the Ethernet driver, note the procedures below. You must know which operating system you are using in your PCM-5824, and then refer to the corresponding installation flow chart. Then just follow the steps described in the flow chart. You will quickly and successfully complete the installation, even if you are not familiar with instructions for MS-DOS or Windows.

7.2.1 Installation for MS-DOS and Windows 3.1

If you want to set up your Ethernet connection under the MS-DOS or Windows 3.1 environment, you should first check your server system model. For example, MS-NT, IBM-LAN server, and so on.

Then choose the correct driver to install in your panel PC.

The installation procedures for various servers can be found on CD-ROM; the correct path being "D:\5824\Ethernet.100\wfw311".
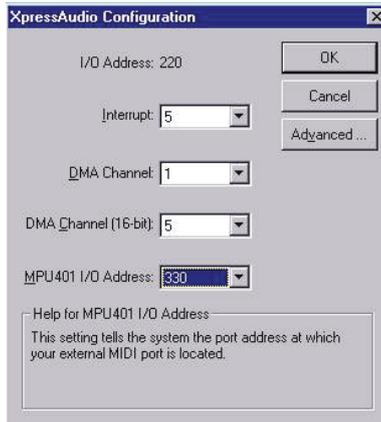
7.2.2 Installation for Windows 95/98

1. a. Select "Start", "Settings". "Control Panel".

    b. Double click "Network".

2. a. Click "Add" and prepare to install network functions.
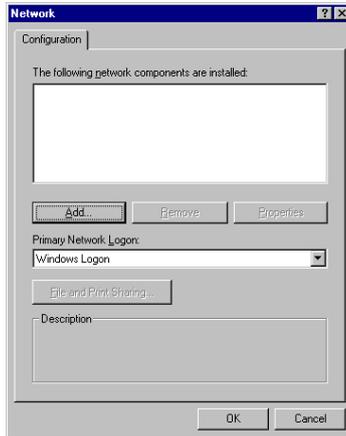
3. a. Select the "Adapter" item to add the Ethernet card.
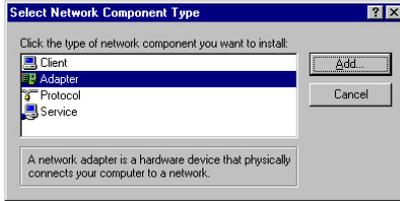
4. a. Click "Have Disk" to install the driver.
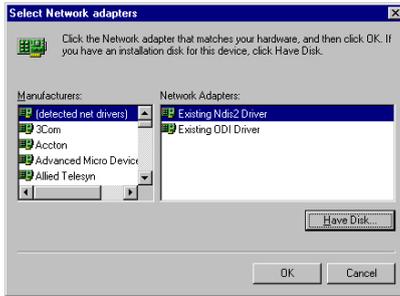
5. a. Insert the CD into the D:\drive.

   b. Fill in "D:\5824\Ethernet.100\Win95\".

   c. Click "OK".

6. a. Choose the "Realtek" item.
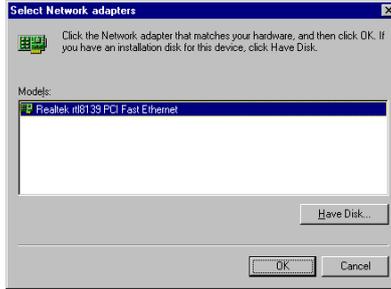
   b. Click "OK".

7. a. Make sure the configurations of relative items are set correctly.

   b. Click "OK" to reboot.
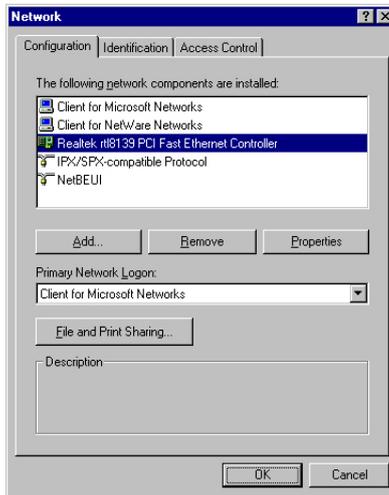
Note: The correct path for Windows 98 is:
*   "D:\5824\Ethernet.100\Win98"*

7.2.3 Installation for Windows NT

1. a. Select "Start", "Settings", "Control Panel".

   b. Double click "Network".
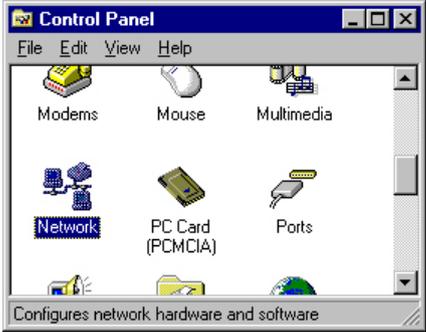
2. a. Choose the "Adapters" label.

   b. Click the "Add" button.
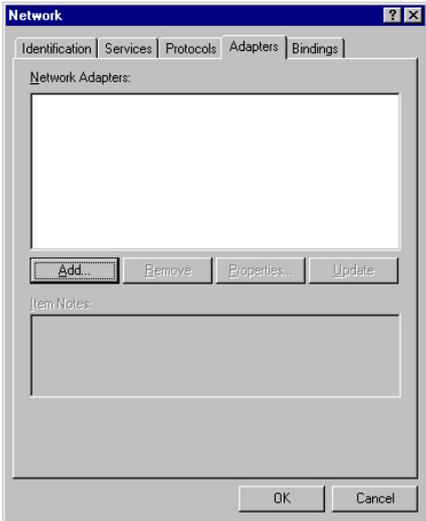
3. a. Press "Have Disk".
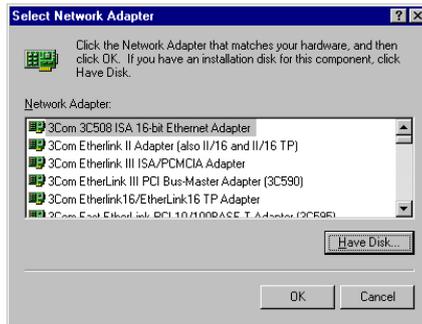
4. a. Type "D"

   b. Press "OK"
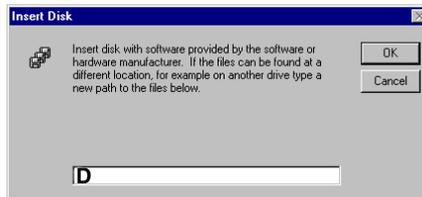
5. a. Insert the CD into D:\drive.

   b. Fill in "D:\5824\Ethernet.100\Winnt\".

   c. Click "OK".
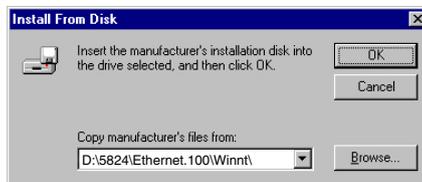
6. a. Choose the "Realtek" item.
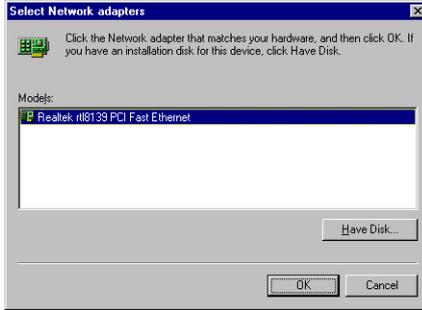
    b. Click "OK".

7. a. Make sure the configurations of relative items are set correctly.

    b. Click "OK" to reboot..
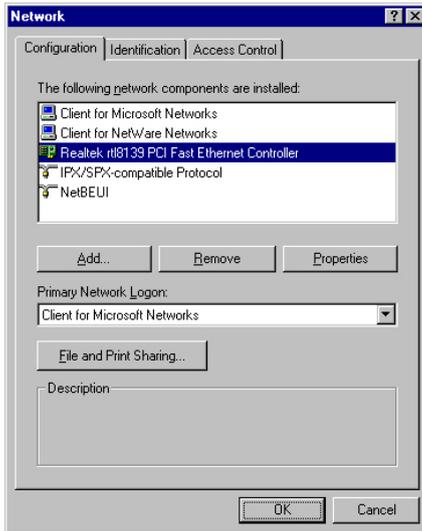
7.3   Further information

Realtek website: **www.realtek.com**

EMAC websites: **www.emacinc.com**

A

Pin Assignments

This appendix contains information of a detailed or specialized nature. It includes:

- CRT display connector
- COM2 RS-232/422/485 serial port connector
- Keyboard and mouse connector
- Main power connector
- IDE hard drive connector
- COM1 RS-232 serial port
- Ethernet 10/100 Mbps connector
- Auxilary power connector
- Floppy drive connector
- Parallel port connector
- IR connector
- USB connector
- Audio connector
- CD audio connector
- CPU fan power connector
- 24 bit LCD connector
- External LCD connector
- CompactFlash Card connector

A.1 CRT display connector (CN18)

Table A-1: CRT display connector

Pin	Signal	Pin	Signal
1	RED	9	VDDC
2	GREEN	10	GND
3	BLUE	11	N/C
4	N/C	12	DDCSDA
5	GND	13	H-SYNC
6	GND	14	V-SYNC
7	GND	15	DDCSCL
8	GND		

A.2 COM2 RS-232/422/485 serial port (CN14)
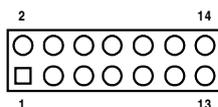
Table A-2: COM2 RS-232/422/485 series port

Pin	RS-232 port	RS-422 port	RS-485 port
1	DCD	N/C	N/C
2	DSR	N/C	N/C
3	RxD	N/C	N/C
4	RTS	N/C	N/C
5	TxD	N/C	N/C
6	CTS	N/C	N/C
7	DTR	N/C	N/C
8	RI	N/C	N/C
9	GND	N/C	N/C
10	N/C	N/C	N/C
11	N/C	TxD+	DATA+
12	N/C	TxD-	DATA-
13	N/C	RxD+	N/C
14	N/C	RxD-	N/C

A.3 Keyboard and mouse connnector (CN17)

Table A-3: Keyboard and mouse connector

Pin	Signal
1	KB DATA
2	MS DATA
3	GND
4	V_{CC}
5	KB CLOCK
6	MS CLOCK

A.4 Main power connector (CN9)

Table A-4: Main power connector

Pin	Signal
1	+12 V
2	GND
3	GND
4	+5 V
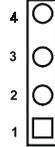
A.5 IDE hard drive connector (CN11)

Table A-5: IDE hard drive connector

Pin	Signal	Pin	Signal
1	IDE RESET*	2	GND
3	DATA 7	4	DATA 8
5	DATA 6	6	DATA 9
7	DATA 5	8	DATA 10
9	DATA 4	10	DATA 11
11	DATA 3	12	DATA 12
13	DATA 2	14	DATA 13
15	DATA 1	16	DATA 14
17	DATA 0	18	DATA 15
19	SIGNAL GND	20	N/C
21	DRQ*	22	GND
23	IO WRITE*	24	GND
25	IO READ*	26	GND
27	IO CHANNEL READY	28	N/C
29	ACK	30	GND
31	IRQ14 (IDE IRQ)	32	IOCS16*
33	ADDR 1	34	N/C
35	ADDR 0	36	ADDR 2
37	HARD DISK SELECT 0	38	HARD DISK SELECT 1
39	IDE ACTIVE*	40	GND
41	VCC	42	VCC
43	GND	44	N/C
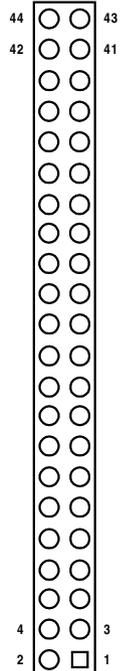
* low active

A.6 COM1 RS-232 serial port (CN16)

Table A-6: COM1 RS-232 serial port

Pin	Signal
1	DCD
2	RxD
3	TxD
4	DTR
5	GND
6	DSR
7	RTS
8	CTS
9	RI

A.7 Ethernet 100Base-T connector (CN13)

Table A-7: Ethernet 100Base-T connector

Pin	Signal
1	XMT+
2	XMT-
3	RCV+
4	N/C
5	N/C
6	RCV-
7	N/C
8	N/C

A.8 Auxilary peripheral power connector (CN10)

```
3
2
1
```

Table A-8: Peripheral power connector

Pin	Signal
3	-12 V
2	GND
1	-5 V

A.9 Floppy drive connector (CN2)

```
33  31                                    3   1
O O O O O O O O O O O O O O O O □
O O O O O O O O O O O O O O O O O
34  32                                    4   2
```
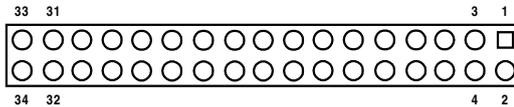
Table A-9: Floppy drive connector

Pin	Signal	Pin	Signal
1	GND	2	DENSITY SELECT*
3	GND	4	N/C
5	GND	6	N/C
7	GND	8	INDEX*
9	GND	10	MOTOR 0*
11	GND	12	DRIVE SELECT 1*
13	GND	14	DRIVE SELECT 0*
15	GND	16	MOTOR 1*
17	GND	18	DIRECTION*
19	GND	20	STEP*
21	GND	22	WRITE DATA*
23	GND	24	WRITE GATE*
25	GND	26	TRACK 0*
27	GND	28	WRITE PROTECT*
29	GND	30	READ DATA*
31	GND	32	HEAD SELECT*
33	GND	34	DISK CHANGE*

* low active

A.10 Parallel port connector (CN3)

```
25  23                              3   1
○ ○ ○ ○ ○ ○ ○ ○ ○ ○ ○ ○ □
○ ○ ○ ○ ○ ○ ○ ○ ○ ○ ○ ○ ○
26  24                              4   2
```
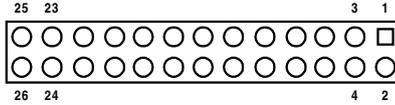
Table A-10: Parallel port connector

Pin	Signal
1	STROBE*
2	AUTOFD*
3	D0
4	ERROR*
5	D1
6	INIT*
7	D2
8	SLCTINI*
9	D3
10	GND
11	D4
12	GND
13	D5
14	GND
15	D6
16	GND
17	D7
18	GND
19	ACK*
20	GND
21	BUSY
22	GND
23	PE
24	GND
25	SLCT
26	GND

* low active

A.11 IR connector (CN7)

Table A-11: IR connector

Pin	Signal
1	+5 V (VCC)
2	N/C
3	IR_RX
4	GND
5	IR_TX

A.12 USB connector (CN4)
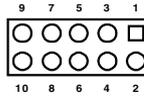
Table A-12: USB connector

Pin	Signal	Pin	Signal
1	USBVCC (5 V)	2	USB VCC (5 V)
3	DATA 0-	4	DATA 1-
5	DATA 0+	6	DATA 1+
7	GND	8	GND
9	GND	10	N/C

A.13 Audio connector (CN5)
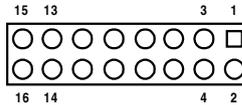
Table A-13: Audio connector

Pin	Signal	Pin	Signal
1	SPEAKER OUT R+	2	SPEAKER OUT R-
3	SPEAKER OUT L+	4	SPEAKER OUT L-
5	LINE OUT R	6	LINE OUT L
7	GND	8	GND
9	LINE IN R	10	LINE IN L
11	GND	12	GND
13	NC	14	NC
15	MIC IN	16	GND

A.14 CD audio connector (CN1)

1	2	3	4
□	○	○	○
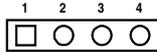
Table A-14: CD audio connector

Pin	Signal
4	GND
3	CD audio control R
2	GND
1	CD audio control L

A.15 CPU fan power connector (CN6)

○	3
○	2
□	1

Table A-15: CPU fan power connector

Pin	Signal
1	+5 V
2	GND
3	+12 V

A.16 24-bit LCD display connector (CN21)
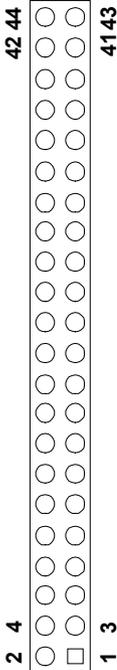
Table A-16: Flat panel connector (CN21)

Pin	Function	Pin	Function
1	+12 V	2	+12 V
3	GND	4	GND
5	3.3 V / 5 V (VDD SAFE)**	6	3.3 V / 5 V (VDD SAFE)**
7	ENAVEE*	8	GND
9	P0	10	P1
11	P2	12	P3
13	P4	14	P5
15	P6	16	P7
17	P8	18	P9
19	P10	20	P11
21	P12	22	P13
23	P14	24	P15
25	P16	26	P17
27	P18	28	P19
29	P20	30	P21
31	P22	32	P23
33	GND	34	GND
35	SHFCLK	36	FLM
37	M	38	LPP
39	GND	40	ENABKL*
41	GND	42	N/C
43	ENAVDD*	44	3.3 V / 5 V (VDD SAFE)**

* high active

** 3.3 V / 5 V can be set via J6

A.17 External LCD display connector (CN22)
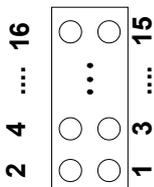
Table A-22: Ext. flat panel connector (CN22)

Pin	Signal	Pin	Signal
1	VDD SAFE	2	VCC
3	P24	4	P25
5	P26	6	P27
7	P28	8	P29
9	P30	10	P31
11	P32	12	P33
13	P34	14	P35
15	GND	16	GND

A.18 CompactFlash™ connector (CN19)

Table A-18: CompactFlash™ card connector (CN24)

Pin	Signal	Pin	Signal
1	GND	2	D03
3	D04	4	D05
5	D06	6	D07
7	*CS0	8	A10
9	*ATA SEL	10	A09
11	A08	12	A07
13	+5 V	14	A06
15	A05	16	A04
17	A03	18	A02
19	A01	20	A00
21	D00	22	D01
23	D02	24	-IOCS16
25	*CD2	26	-CD1
27	D11	28	D12
29	D13	30	D14
31	D15	32	-CS1
33	*VS1	34	-IORD
35	*IOWR	36	-WE
37	INTRQ	38	+5 V
39	*CSEL	40	-VS2
41	*RESER	42	IORDY
43	*INPACK	44	-REG
45	*DASP	46	-PDIAG
47	D08	48	D09
49	D10	50	GND

* low active

System Assignments

- System I/O ports
- DMA channel assignments
- Interrupt assignments
- 1st MB memory map

B.1 System I/O ports

Table B-1: System I/O ports

Addr. range (Hex)	Device
000-01F	DMA controller
020-021	Interrupt controller 1, master
022-023	Chipset address
040-05F	8254 timer
060-06F	8042 (keyboard controller)
070-07F	Real-time clock, non-maskable interrupt (NMI) mask
080-09F	DMA page register,
0A0-0BF	Interrupt controller 2
0C0-0DF	DMA controller
0F0	Clear math co-processor
0F1	Reset math co-processor
0F8-0FF	Math co-processor
170- 178	2nd fixed disk for CompactFlash
1F0-1F8	1st fixed disk
200-207	Game I/O
278-27F	Reserved
2F8-2FF	Serial port 2
300-31F	Ethernet**
360-36F	Reserved
378-37F	Parallel printer port 1 (LPT2)
380-38F	SDLC, bisynchronous 2
3A0-3AF	Bisynchronous 1
3B0-3BF	Monochrome display and printer adapter (LPT1)
3C0-3CF	Reserved
3D0-3DF	Color/graphics monitor adapter
3F0-3F7	Diskette controller
3F8-3FF	Serial port 1
443	Watchdog timer

* PNP audio I/O map range from 220 ~ 250H (16 bytes)
  MPU-401 select from 300 ~ 330H (2 bytes)

** default setting

B.2 DMA channel assignments

Table B-2: DMA channel assignments

Channel	Function
0	Available
1	Audio*
2	Floppy disk (8-bit transfer)
3	Parallel**
4	Cascade for DMA controller 1
5	Audio*
6	Available
7	Available

* Audio DMA default setting: DMA 1.5

Audio High DMA select: DMA 1.3

Audio Low DMA select: DMA 5.6.7

** Parallel port DMA default setting: DMA 3

Parallel port DMA select: DMA 1.3

B.3 Interrupt assignments

Table B-3: Interrupt assignments

Interrupt#	Interrupt source
NMI	Parity error detected
IRQ 0	Interval timer
IRQ 1	Keyboard
IRQ 2	Interrupt from controller 2 (cascade)
IRQ 8	Real-time clock
IRQ 9	Reserve
IRQ 10	Available
IRQ 11	Reserved for watchdog timer
IRQ 12	PS/2 mouse
IRQ 13	INT from co-processor
IRQ 14	Preliminary IDE
IRQ 15	Secondary IDE for CompactFlash
IRQ 3	Serial communication port 2
IRQ 4	Serial communication port 1
IRQ 5	Audio*
IRQ 6	Diskette controller (FDC)
IRQ 7	Parallel port 1 (print port)

* Audio default setting: IRQ5
  USB and Ethernet IRQ is automatically set by the system

B.4 1st MB memory map

Table B-4: 1st MB memory map

Addr. range (Hex)	Device
F000h - FFFFh	System ROM
D800h - EFFFh	Unused
D000 - D400H	Available
C800h - D7FFh	Ethernet ROM*
C000h - C7FFh	VGA BIOS
B800h - BFFFh	CGA/EGA/VGA text
B000h - B7FFh	Reserved for graphic mode usage
A000h - AFFFh	EGA/VGA graphics
0000h - 9FFFh	Base memory

* default setting

Installing PC/104 Modules

This appendix gives instructions for installing PC/104 modules.

C.1 Installing PC/104 modules

The PCM-5824's PC/104 connectors give you the flexibility to attach PC/104 modules.

Installing these modules on the PCM-5824 is quick and simple. The following steps show how to mount the PC/104 modules:

1. Remove the PCM-5824 from your system, paying particular attention to the safety instructions already mentioned above.

2. Make any jumper or link changes required to the CPU card now. Once the PC/104 module is mounted you may have difficulty in accessing these.

3. Normal PC/104 modules have male connectors and mount directly onto the main card. (Refer to the diagram on the following page.)

4. Mount the PC/104 module onto the CPU card by pressing the module firmly but carefully onto the mounting connectors.

5. Secure the PC/104 module onto the CPU card using the four mounting spacers and screws.
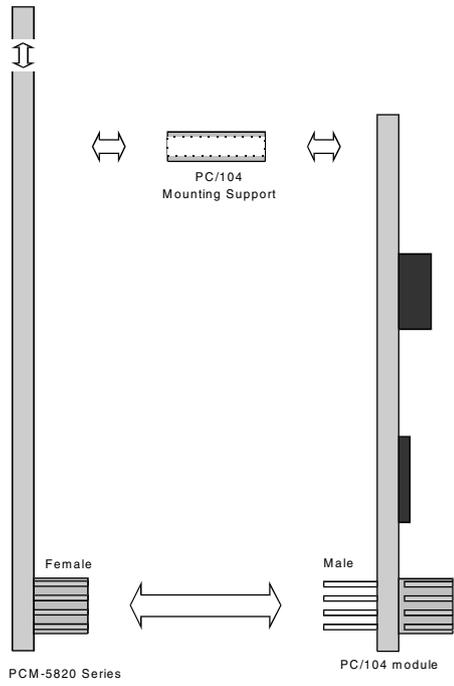
Figure C-1: PC/104 module mounting diagram
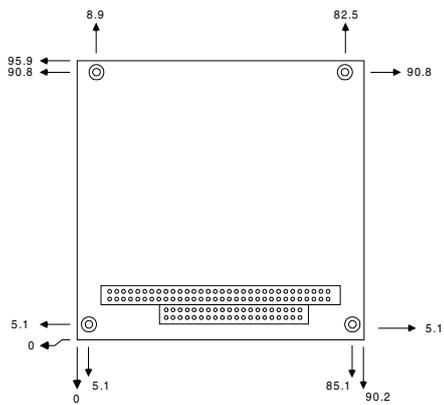
Figure C-2: PC/104 module dimensions (mm) (±0.1)

Table C-1: PC/104 connectors (CN8)

Pin Number	Signal Row A	Row B	Signal Row C	Row D
0	—	—	0 V	0 V
1	IOCHCHK*	0 V	SBHE*	MEMCS16*
2	SD7	RESETDRV	LA23	IOCS16*
3	SD6	+5 V	LA22	IRQ10
4	SD5	IRQ9	LA21	IRQ11
5	SD4	-5 V	LA20	IRQ12
6	SD3	DRQ2	LA19	IRQ15
7	SD2	-12 V	LA18	IRQ14
8	SD1	ENDXFR*	LA17	DACK0*
9	SD0	+12 V	MEMR*	DRQ0
10	IOCHRDY	N/C	MEMW*	DACK5*
11	AEN	SMEMW*	SD8	DRQ5
12	SA19	SMEMR*	SD9	DACK6*
13	SA18	IOW*	SD10	DRQ6
14	SA17	IOR*	SD11	DACK7*
15	SA16	DACK3*	SD12	DRQ7
16	SA15	DRQ3	SD13	+5 V
17	SA14	DACK1*	SD14	MASTER*
18	SA13	DRQ1	SD15	0 V
19	SA12	REFRESH*	KEY	0 V
20	SA11	SYSCLK	—	—
21	SA10	IRQ7	—	—
22	SA9	IRQ6	—	—
23	SA8	IRQ5	—	—
24	SA7	IRQ4	—	—
25	SA6	IRQ3	—	—
26	SA5	DACK2*	—	—
27	SA4	TC	—	—
28	SA3	BALE	—	—
29	SA2	+5 V	—	—
30	SA1	OSC	—	—
31	SA0	0 V	—	—
32	0 V	0 V	—	—

* active low

D

Programming the Watchdog Timer

The PCM-5824 is equipped with a watchdog timer that resets the CPU or generates an interrupt if processing comes to a standstill for whatever reason. This feature ensures system reliability in industrial standalone or unmanned environments.

D.1   Programming the watchdog timer

Jumper J3 controls the watchdog settings. The default configuration of the timer is enabled via a system reset.

To enable the watchdog timer, you must write a program which writes 1 to I/O port address 443 (hex) at regular intervals. The first time your program reads the port, it enables the watchdog timer. After that, your program must write 1 to the port at time interval of less than 1.6 seconds, otherwise the watchdog timer will activate and reset the CPU or generate an interrupt on IRQ11. When you want to disable the watchdog timer, your program should write 0 to I/O port 443.

If CPU processing comes to a standstill because of EMI or a software bug, your program's signals to I/O port 443 to the timer will be interrupted. The timer will then automatically reset the CPU or invoke an IRQ, and data processing will continue normally.

You must write your program so that it writes 1 to I/O port 443 at an interval shorter than the timer's preset interval. The timer's intervals have a tolerance of ± 30%, so you should program an instruction that will refresh the timer about every second.

The following program shows how you might program the
watchdog timer in BASIC:

```
10       REM  Watchdog timer example program

20       X=Out &H443, 1 REM  Enable and refresh the
         watchdog

30       GOSUB 1000 REM  Task #1, takes 1 second to
         complete

40       X=Out &H443, 1 REM  Refresh the watchdog

50       GOSUB 2000 REM  Task #2, takes 1 second to
         complete

60       X=Out &H443, 0 REM  Disable the watchdog

70       END

1000     REM  Subroutine #1, takes 1 second to
         complete
  .
  .
  .
1070     RETURN

2000     REM  Subroutine #2, takes 1 second to
         complete
  .
  .
  .
2090     RETURN
```

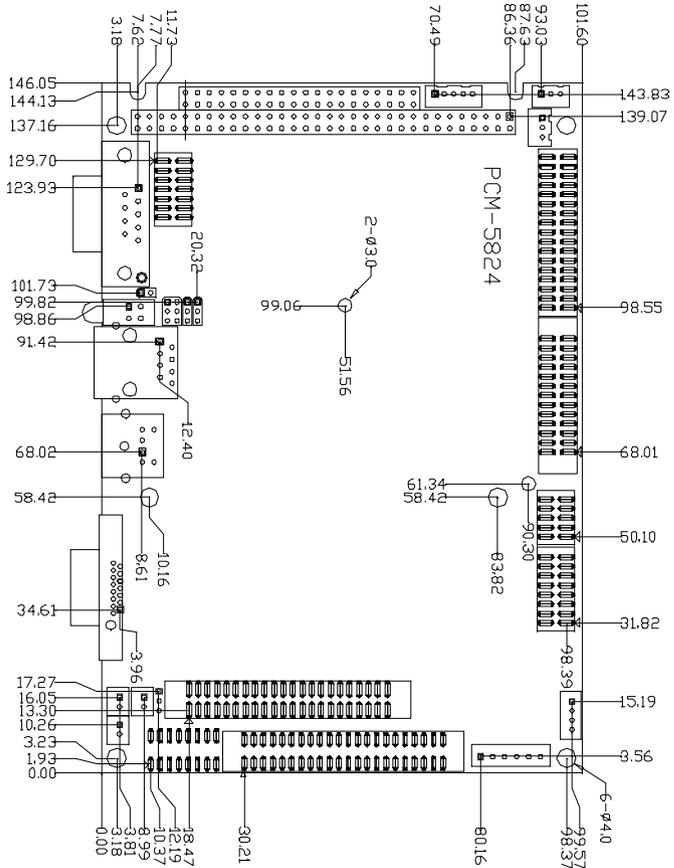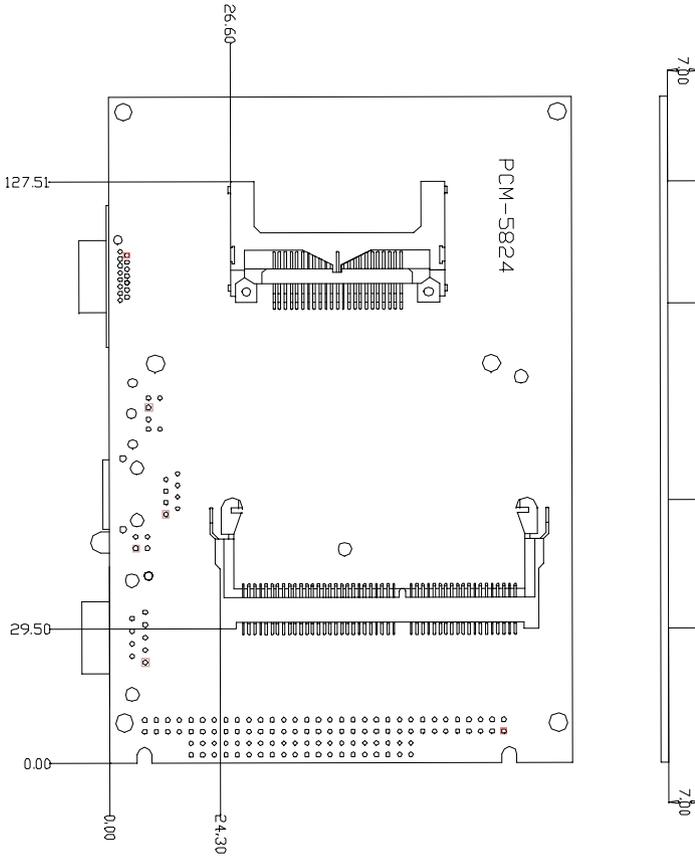# Mechanical Drawings

E.1 Component side

Figure E-1: Board Dimensions component side

E.2 Solder side

Figure F-3: Board Dimensions solder side (PCM-5824/PCM-5820 Series)